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- Designed for Complementary Use with BDW84, BDW84A, BDW84B, BDW84C and BDW84D
- 150 W at 25°C Case Temperature
- 15 A Continuous Collector Current
- Minimum h_{FE} of 750 at 3 V, 6 A

SOT-93 PACKAGE (TOP VIEW) B 1 C 2 3

Pin 2 is in electrical contact with the mounting base.

MDTRAA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING	SYMBOL	VALUE	UNIT		
	BDW83		45		
	BDW83A		60		
Collector-base voltage (I _E = 0)	BDW83B	V_{CBO}	80	V	
	BDW83C		100		
	BDW83D		120		
	BDW83		45		
	BDW83A		60		
Collector-emitter voltage (I _B = 0) (see Note 1)	BDW83B	V_{CEO}	80	V	
	BDW83C		100		
	BDW83D		120		
Emitter-base voltage	V _{EBO}	5	V		
Continuous collector current	I _C	15	Α		
Continuous base current	I _B	0.5	Α		
Continuous device dissipation at (or below) 25°C case temperature (see Note 2	P _{tot}	150	W		
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			3.5	W	
Unclamped inductive load energy (see Note 4)	½Ll _C ²	100	mJ		
Operating junction temperature range	T _j	-65 to +150	°C		
Operating temperature range	T _{stg}	-65 to +150	°C		
Operating free-air temperature range	T _A	-65 to +150	°C		

NOTES: 1. These values apply when the base-emitter diode is open circuited.

- 2. Derate linearly to 150°C case temperature at the rate of 1.2 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 28 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = 5 mA, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_S = 0.1 Ω , V_{CC} = 20 V.



BDW83, BDW83A, BDW83B, BDW83C, BDW83D NPN SILICON POWER DARLINGTONS

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electrical characteristics at 25°C case temperature (unless otherwise noted)

	PARAMETER		TEST	CONDITIONS		MIN	TYP	MAX	UNIT
V _{(BR)CEO}	Collector-emitter	I _C = 30 mA	I _B = 0	(see Note 5)	BDW83 BDW83A BDW83B	45 60 80			V
	3				BDW83C BDW83D	100 120			
	Collector-emitter	V _{CE} = 30 V	$I_B = 0$		BDW83			1	
		$V_{CE} = 30 V$	$I_B = 0$		BDW83A			1	
I_{CEO}	cut-off current	$V_{CE} = 40 \text{ V}$	$I_B = 0$		BDW83B			1	mA
	cut-on current	$V_{CE} = 50 \text{ V}$	$I_B = 0$		BDW83C			1	
		$V_{CE} = 60 \text{ V}$	$I_B = 0$		BDW83D			1	
		$V_{CB} = 45 \text{ V}$	$I_E = 0$		BDW83			0.5	
		$V_{CB} = 60 \text{ V}$	$I_E = 0$		BDW83A			0.5	
		$V_{CB} = 80 \text{ V}$	$I_E = 0$		BDW83B			0.5	
		$V_{CB} = 100 \text{ V}$	$I_E = 0$		BDW83C			0.5	
I _{CBO}	Collector cut-off current	$V_{CB} = 120 \text{ V}$	$I_E = 0$		BDW83D			0.5	mA
,CBO		~-	$I_E = 0$	$T_C = 150$ °C	BDW83			5	1117 (
		~-	$I_E = 0$	· ·	BDW83A			5	
			$I_E = 0$	$T_C = 150$ °C	BDW83B			5	
		~-	$I_E = 0$	$T_C = 150$ °C	BDW83C			5	
		$V_{CB} = 120 \text{ V}$	$I_E = 0$	$T_C = 150$ °C	BDW83D			5	
I _{EBO}	Emitter cut-off current	V _{EB} = 5 V	$I_C = 0$					2	mA
h _{FE}	Forward current transfer ratio	$V_{CE} = 3 V$ $V_{CE} = 3 V$	$I_{\rm C} = 6 {\rm A}$ $I_{\rm C} = 15 {\rm A}$	(see Notes 5 and 6)		750 100		20000	
V _{BE(on)}	Base-emitter voltage	V _{CE} = 3 V	I _C = 6 A	(see Notes 5 and 6)				2.5	V
V _{CE(sat)}	Collector-emitter saturation voltage	$I_B = 12 \text{ mA}$ $I_B = 150 \text{ mA}$	I _C = 6 A I _C = 15 A	(see Notes 5 and 6)				2.5 4	V
V _{EC}	Parallel diode forward voltage	I _E = 15 A	I _B = 0					3.5	V

NOTES: 5. These parameters must be measured using pulse techniques, t_p = 300 μ s, duty cycle \leq 2%.

thermal characteristics

	PARAMETER	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			0.83	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			35.7	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = 10 A	$I_{B(on)} = 40 \text{ mA}$	$I_{B(off)} = -40 \text{ mA}$		0.9		μs
t _{off}	Turn-off time	$V_{BE(off)} = -4.2 \text{ V}$	$R_L = 3 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		7		μs

 $^{^{\}dagger} \ \ \mbox{Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.}$

PRODUCT INFORMATION

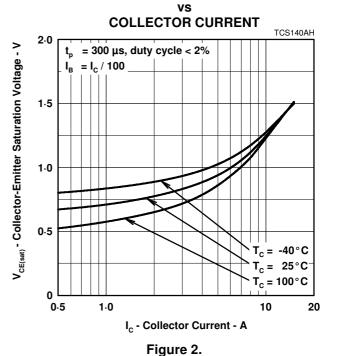
^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN COLLECTOR CURRENT TCS140AG 70000 $T_c = -40$ °C 25°C = 100°C h_{FE} - Typical DC Current Gain 10000 1000 3 V = 300 μs, duty cycle < 2% 100 0.5 1.0 10 20 Ic - Collector Current - A

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE



BASE-EMITTER SATURATION VOLTAGE

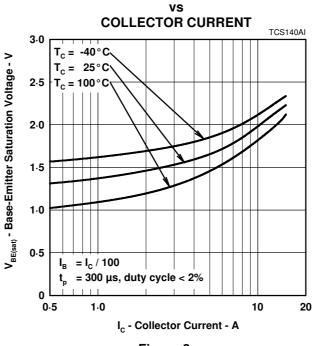
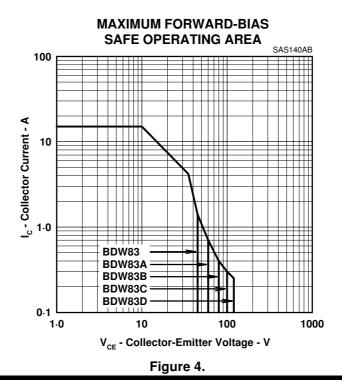


Figure 3.

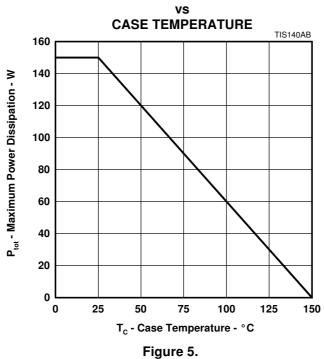


MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION



PRODUCT INFORMATION

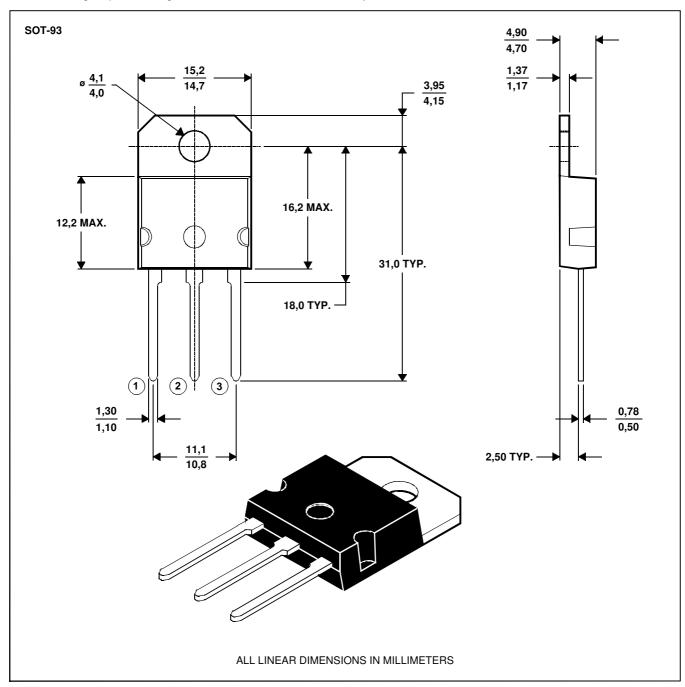
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MECHANICAL DATA

SOT-93

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: The centre pin is in electrical contact with the mounting tab.

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PRODUCT INFORMATION

BDW83, BDW83A, BDW83B, BDW83C, BDW83D NPN SILICON POWER DARLINGTONS

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