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## 1N5908 SM5908

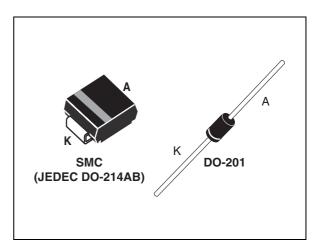
## Transil™

### Features

- Peak pulse power:
  - 1500 W (10/1000 μs)
- Stand off voltage: 5 V
- Unidirectional
- Operating T<sub>j max</sub>: 175 °C
- High power capability at T<sub>j max</sub>:
   − 1500 W (10/1000 µs)
- JEDEC registered package outline

### Complies with the following standards

- IEC 61000-4-2 level 4:
  - 15 kV (air discharge)
  - 8 kV (contact discharge)
- IEC 61000-4-5
- MIL STD 883G, method 3015-7 Class 3B
   25 kV HBM (human body model)
- Resin meets UL 94, V0
- MIL-STD-750, method 2026 solderability
- EIA STD RS-481 and IEC 60286-3 packing
- IPC 7531 footprint



## Description

This Transil series has been designed to protect sensitive equipment against electrostatic discharges according to IEC 61000-4-2, and MIL STD 883, method 3015, and electrical over stress according to IEC 61000-4-4 and 5. These devices are more generally used against surges below 1500 W (10/1000  $\mu$ s).

The Planar technology makes it compatible with high-end equipment and SMPS where low leakage current and high junction temperature are required to provide reliability and stability over time.

They are packaged in SMC (SMC footprint in accordance with IPC 7531 standard) and DO-201.

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## 1 Characteristics

### Table 1. Absolute maximum ratings ( $T_{amb} = 25 \ ^{\circ}C$ )

Symbol	Parameter	Value	Unit		
P <sub>PP</sub>	Peak pulse power dissipation <sup>(1)</sup>	1500	W		
T <sub>stg</sub>	Storage temperature range	-65 to +175	°C		
Тj	Operating junction temperature range -55 to +175 ° C				
TL	Maximum lead temperature for soldering during 10 s.	260	°C		

1. For a surge greater than the maximum values, the diode will fail in short-circuit.

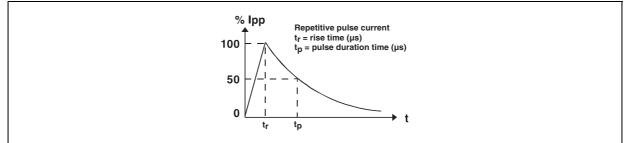
### Table 2. Thermal resistances

Symbol	Parameter	Value	Unit	
D	Junction to leads	SMC	15	
R <sub>th(j-l)</sub>	Sunction to leads	DO-201	20	° C/W
Р	Junction to ambient on printed circuit on recommended pad layout	SMC	90	0.00
R <sub>th(j-a)</sub>	Junction to ambient	DO-201	75	

### Figure 1. Electrical characteristics - definitions

Symbo V <sub>RM</sub> V <sub>BR</sub> V <sub>CL</sub> I <sub>RM</sub> I <sub>PP</sub> aT V <sub>F</sub> R <sub>D</sub>	I Parameter Stand-off voltage Breakdown voltage Clamping voltage Leakage current @ V <sub>RM</sub> Peak pulse current Voltage temperature coefficient Forward voltage drop Dynamic resistance	V <sub>CL</sub> V <sub>BR</sub> V <sub>RM</sub>	V <sub>F</sub> I <sub>RM</sub> I <sub>R</sub>	V	
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### Figure 2. Pulse definition for electrical characteristics





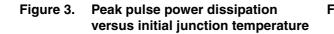
	I <sub>RM</sub> @V <sub>RM</sub>		$V_{BR} @I_{R}^{(1)}$		V <sub>CL</sub> @I <sub>PP</sub> , 10/1000 µs		V <sub>CL</sub> @I <sub>PP</sub> , 10/1000 μs		V <sub>CL</sub> @I <sub>PP</sub> , 10/1000 μs		α <b>Τ <sup>(2)</sup></b>	С
Order code	max		min		max		max		max		max	typ
	μA	v	v	mA	V	A <sup>(3)</sup>	V	A <sup>(3)</sup>	v	A <sup>(3)</sup>	10-4/ °C	pF
1N5908	300	5	6	4	7.6	30	8	60	8.5	120	5.7	9500
SM5908	300	5	0	1	7.0	30	0	00	0.0	120	5.7	9500

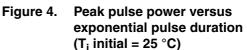
Table 3. Electrical characteristics - parameter values (T<sub>amb</sub> = 25 °C)

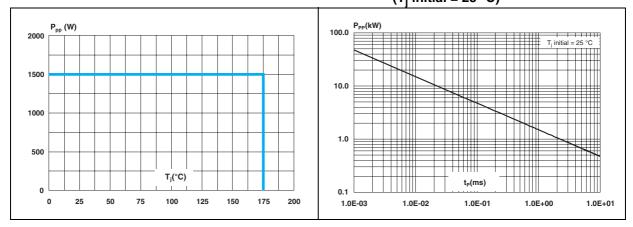
1. Pulse tes: t<sub>p</sub> < 50 ms

2. To calculate V<sub>BR</sub> or V<sub>CL</sub> versus junction temperature, use the following formulas: V<sub>BR</sub> @ T<sub>J</sub> = V<sub>BR</sub> @ 25°C x (1 +  $\alpha$ T x (T<sub>J</sub> - 25)) V<sub>CL</sub> @ T<sub>J</sub> = V<sub>CL</sub> @ 25°C x (1 +  $\alpha$ T x (T<sub>J</sub> - 25))

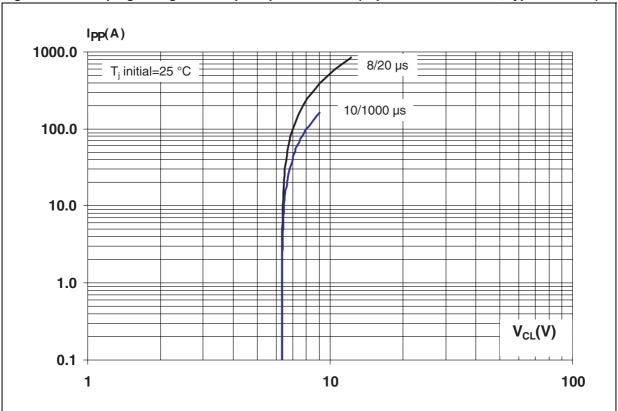
3. Surge capability given for both directions







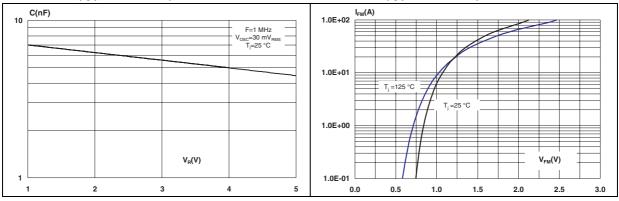




### Figure 5. Clamping voltage versus peak pulse current (exponential waveform, typical values)

## Figure 6. Junction capacitance versus reverse applied voltage (typical values)

### Figure 7. Peak forward voltage drop versus peak forward current (typical values)





Z<sub>th(j-a)</sub> / R<sub>th(j-a)</sub>

Recommended pad layout PCB FR4, copper thickness = 35 µm

1.0E-01

1.0E-02

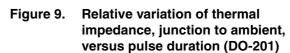
1.00

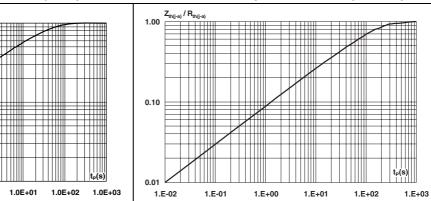
0.10

0.01

1.0E-03

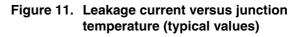
# Figure 8. Relative variation of thermal impedance, junction to ambient, versus pulse duration (SMC)

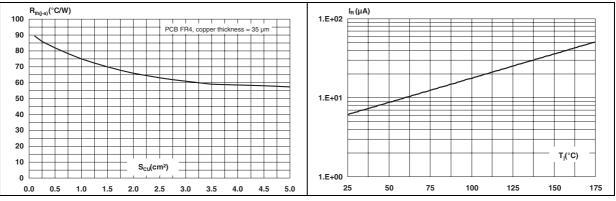




### Figure 10. Thermal resistance junction to ambient versus copper surface under each lead (SMC)

1.0E+00







### 2 Package information

- Case: JEDEC DO-214AB molded plastic over planar junction
- Terminals: solder plated, solderable per MIL-STD-750, Method 2026
- Polarity: for unidirectional types the band indicates cathode
- Flammability: epoxy is rated UL94V-0
- RoHS package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK<sup>®</sup> is an ST trademark.

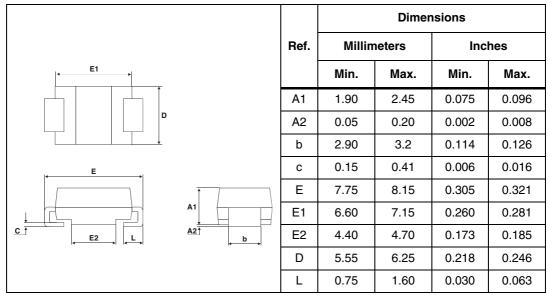
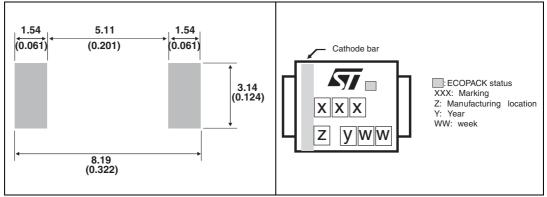


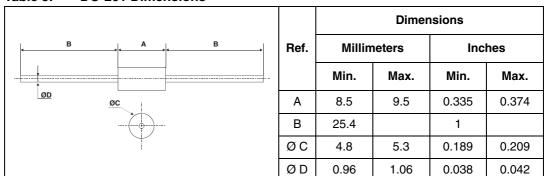
Table 4. SMC dimensions

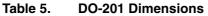
Figure 12. SMC footprint dimensions mm Figure 13. SMC marking layout<sup>(1)</sup> (inches)



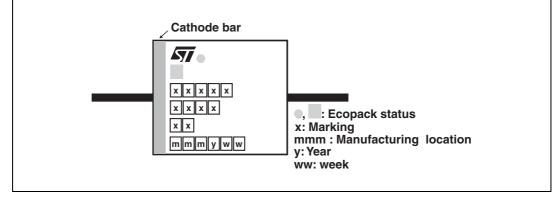
1. Marking layout can vary according to assembly location.













## **3** Ordering information

### Table 7. Ordering information

Order code	Marking	Package	Package Weight		Delivery mode	
SM5908	MDC	SMC	0.25 g	2500	Tape and reel	
1N5908	1N5908	DO-201	0.9 g	600	Ammopack	

## 4 Revision history

### Table 8.Document revision history

Date	Revision	Changes
Aug-1999	2A	Previous release
20-Sep-2011	3	Added cathode bands. Added standards compliance statements. Updated <i>Description</i> . Updated <i>Table 1</i> and <i>Table 2</i> . Updated Figures 3 through 11. Updated <i>Section 2: Package</i> <i>information</i> .



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