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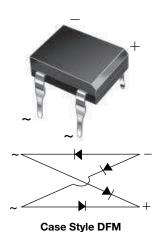


# DF005M, DF01M, DF02M, DF04M, DF06M, DF08M, DF10M

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Vishay General Semiconductor

# Miniature Glass Passivated Single-Phase Bridge Rectifiers



| PRIMARY CHARACTERISTICS                  |  |  |  |  |  |  |  |
|--|--|--|--|--|--|--|--|
| Package                                  | DFM  |  |  |  |  |  |  |
| I <sub>F(AV)</sub>                       | 1 A  |  |  |  |  |  |  |
| V <sub>RRM</sub>                         | 50 V, 100 V, 200 V, 400 V, 600 V,<br>800 V, 1000 V |  |  |  |  |  |  |
| I <sub>FSM</sub>                         | 50 A   |  |  |  |  |  |  |
| I <sub>R</sub>                           | 5 μΑ   |  |  |  |  |  |  |
| V <sub>F</sub> at I <sub>F</sub> = 1.0 A | 1.1 V  |  |  |  |  |  |  |
| T <sub>J</sub> max.                      | 150 °C   |  |  |  |  |  |  |
| Diode variations                         | Quad   |  |  |  |  |  |  |

#### **FEATURES**





- · Ideal for printed circuit boards
- Applicable for automative insertion



- High surge current capability
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **TYPICAL APPLICATIONS**

General purpose use in AC/DC bridge full wave rectification for SMPS, lighting ballaster, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

#### **MECHANICAL DATA**

Case: DFM

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked on body

| MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)        |                                   |                                  |       |       |       |       |       |                  |      |
|--|-----------------------------------|----------------------------------|-------|-------|-------|-------|-------|------------------|------|
| PARAMETER  | SYMBOL                            | DF005M                           | DF01M | DF02M | DF04M | DF06M | DF08M | DF10M            | UNIT |
| Device marking code  |                                   | DF005                            | DF01  | DF02  | DF04  | DF06  | DF08  | DF10             |      |
| Maximum repetitive peak reverse voltage                                | $V_{RRM}$                         | 50                               | 100   | 200   | 400   | 600   | 800   | 1000             | V    |
| Maximum RMS voltage  | V <sub>RMS</sub>                  | 35                               | 70    | 140   | 280   | 420   | 560   | 700              | >    |
| Maximum DC blocking voltage  | $V_{DC}$                          | 50                               | 100   | 200   | 400   | 600   | 800   | 1000             | ٧    |
| Maximum average forward output rectified current at $T_A$ = 40 °C      | I <sub>F(AV)</sub>                | 1.0                              |       |       |       |       |       |                  | Α    |
| Peak forward surge current single sine-wave superimposed on rated load | I <sub>FSM</sub>                  | 50                               |       |       |       |       |       | Α                |      |
| Rating for fusing (t < 8.3 ms)   | l <sup>2</sup> t                  | l <sup>2</sup> t 10              |       |       |       |       |       | A <sup>2</sup> s |      |
| Operating junction and storage temperature range                       | T <sub>J</sub> , T <sub>STG</sub> | , T <sub>STG</sub> - 55 to + 150 |       |       |       |       |       |                  | °C   |

| <b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted) |                         |                   |        |       |       |       |       |          |       |      |
|---|-------------------------|-------------------|--------|-------|-------|-------|-------|----------|-------|------|
| PARAMETER   | TEST CONDITIONS         | SYMBOL            | DF005M | DF01M | DF02M | DF04M | DF06M | DF08M    | DF10M | UNIT |
| Maximum instantaneous forward voltage drop per diode                              | 1.0 A                   | V <sub>F</sub>    | 1.1    |       |       |       |       | <b>V</b> |       |      |
| Maximum reverse current at  | T <sub>A</sub> = 25 °C  |                   | 5.0    |       |       |       |       |          |       | μA   |
| rated DC blocking voltage per diode   | T <sub>A</sub> = 125 °C | IR                | 500    |       |       |       |       |          |       |      |
| Typical junction capacitance per diode  | 4.0 V, 1 MHz            | C <sub>J</sub> 25 |        |       |       | pF    |       |          |       |      |



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| THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted) |                 |        |       |       |       |       |       |       |      |
|---|-----------------|--------|-------|-------|-------|-------|-------|-------|------|
| PARAMETER   | SYMBOL          | DF005M | DF01M | DF02M | DF04M | DF06M | DF08M | DF10M | UNIT |
| Typical thermal resistance (1)  | $R_{\theta JA}$ | 40     |       |       |       |       |       | °C/W  |      |
| Typical trieffial resistance (9   | $R_{\theta JL}$ | 15     |       |       |       |       |       |       | C/VV |

#### Note

<sup>(1)</sup> Thermal resistance from junction to ambient and from junction to lead mounted on PCB with 0.5" x 0.5" (13 mm x 13 mm) copper pads

| ORDERING INFORMATION (Example) |                 |                        |               |               |  |  |  |  |
|--------------------------------|-----------------|------------------------|---------------|---------------|--|--|--|--|
| PREFERRED P/N                  | UNIT WEIGHT (g) | PREFERRED PACKAGE CODE | BASE QUANTITY | DELIVERY MODE |  |  |  |  |
| DF06M-E3/45                    | 0.416           | 45                     | 50            | Tube          |  |  |  |  |

### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

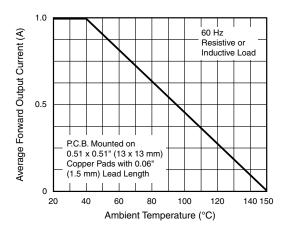


Fig. 1 - Derating Curve Output Rectified Current

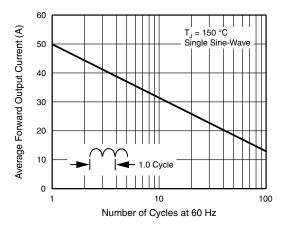


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

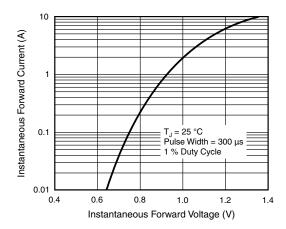


Fig. 3 - Typical Forward Characteristics Per Diode

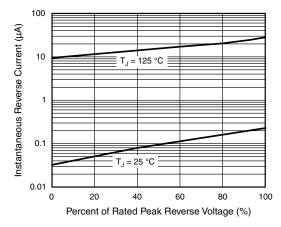


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode



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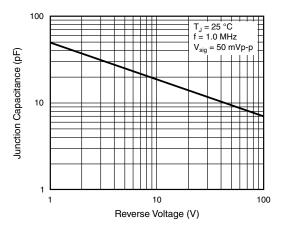


Fig. 5 - Typical Junction Capacitance Per Diode

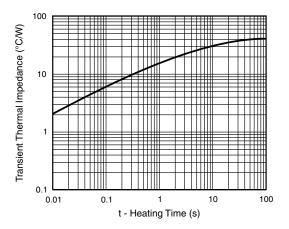
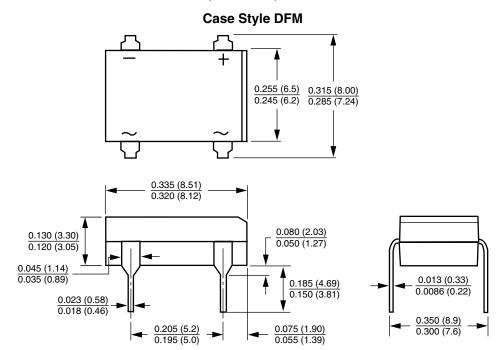


Fig. 6 - Typical Transient Thermal Impedance

### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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