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L50Slim GPS Module Integrated Patch Antenna







Embedded Patch Antenna



Self-Assisted CGEE



Low Power Consumption



Super Sensitivity



High Accuracy



Extended
Temperature Range



Anti-Jamming



SMD Type



RoHS Compliant

L50 is an ultra slim module with embedded 15 × 15 × 2.0mm patch antenna. It is built upon the SiRFstarIV $^{\text{TM}}$ chip solution which offers high performance GPS engine. Alongside highest reliability and quality of patch antenna, L50 also offers 48 PRN channels, which allows the module to acquire and track satellites in the shortest time, even at a very low signal level. L50 is also compatible with 18 ×18 × 2.0mm patch antenna. Its highly compact design with minimal patch antenna perfectly suits portable devices, asset tracking, connected PND, security devices, vehicle management and other industry applications.

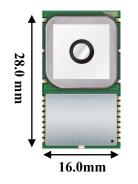
L50 supports A-GPS function without the necessity of data download from server since it captures ephemeris data from satellites locally and predicts ephemeris out to 3 days.

Advantages

- Embedded patch antenna
 - Default size 15 ×15 × 2.0mm
 - Alternative size 18 ×18 × 2.0mm
- Self-Assisted CGEE: Out to 3 days of prediction
- Extremely low power mode: Hibernate mode, 36uW@1.8V
- Low tracking power consumption: 38mA @ -130dBm
- Super sensitivity
 - Highest acquisition sensitivity: -148dBm
 - Highest tracking sensitivity: -163dBm
- 48 PRN channels
- Ultra slim form factor: 28 × 16 × 3.0mm(with patch antenna)
- Active Jammer Remover
 - Removes in-band jammers up to 80 dB-Hz
 - Tracks up to 8 CW jammers

L50

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General Specifications

L1 Band Receiver	Channel	48 channels	Dynamic	Maximum	18288 m
(1575.42MHz)	C/A code		Performance	Altitude	
	SBAS	WAAS, EGNOS		Maximum Velocity	514m/s
Horizontal Position	Autonomous	<2.5 m CEP		Maximum	4 G
Accuracy	SBAS	<2.0 m CEP		Acceleration	
Velocity Accuracy	Without aid	<0.01 m/s	Dimensions	28 × 16 × 3.0mm	1
Acceleration Accuracy	Without aid	0.1 m/s ²	Weight	Approx. 4.0g	
Timing Accuracy		<100 ns			

Sorial Interfaces

Reacquisition Time)	<1 s	Serial Interfa	ces	
TTFF	Cold Start	<33s	One Multiplexed	UART: Adjustable 1200~115200 bps	
(Time To First Fix)	Warm Start	<33s	Interface	Default: 4800bps	
	Warm Start 10s		IIC (master/slave): Up to 400 Kbps		
	with CGEE		I/O Voltage	1.71V ~ 1.89V	
Sensitivity *	Hot Start	<1s	Protocols	NMEA OSP	
	Acquisition	-148dBm			
	Hot Start	-160dBm	Power Management		
	Tracking	-163dBm			

MHZ	Acquisition	
	Power Tracking	38mA @-130dBm
z min	Power Saving	ATP, PTF, Hibernate
c typ.	J	
v		

Power supply

Power

Contact Us

Address	Room 501, Building 13, No.99 Tian- Zhou Road, Shanghai, China

1.71V ~ 1.89V

48mA @-130dBm

Performance

Environmental

Range Of Receiving Frequency

Navigation

±1.023MH

-160dBm

1575.42MHz

Band Width 10MHz Gain at Zenith 1.0dBic **VSWR** 1.5 max **Axial Ratio** -3dB max **RHCP** Polarization 50 Ohm Impendence 0±20ppm/°C

Frequency Temperature

Coefficient

Operating -40°C to 85°C

Temperature

Storage -45°C to 125°C

Temperature













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Patch Antenna

^{*} Measured in conducted method by 8-star GPS simulator