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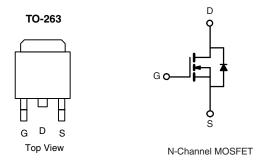
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Vishay Siliconix

# Automotive N-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	40				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0021				
I <sub>D</sub> (A)	120				
Configuration	Single				



#### **FEATURES**

- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance
- 100 % R<sub>q</sub> and UIS Tested
- AEC-Q101 Qualified<sup>d</sup>
- Material categorization:
  For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>





ROHS COMPLIANT HALOGEN FREE

ORDERING INFORMATION	
Package	TO-263
Lead (Pb)-free and Halogen-free	SQM120N04-2m1-GE3

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V <sub>DS</sub>	40	.,		
Gate-Source Voltage	V <sub>GS</sub>	± 20	V		
Continuous Drain Current <sup>a</sup>	T <sub>C</sub> = 25 °C	1	120		
	T <sub>C</sub> = 125 °C	I <sub>D</sub>	120		
Continuous Source Current (Diode Conduct	I <sub>S</sub>	120	Α		
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	480			
Single Pulse Avalanche Current	1 0.1 ml l	I <sub>AS</sub>	84		
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	352	mJ	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	D	300	14/	
	T <sub>C</sub> = 125 °C	- P <sub>D</sub>	100	W	
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient F	PCB Mount <sup>c</sup>	$R_{thJA}$	40	°C/W		
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.5	G/W		

#### Notes

- a. Package limited.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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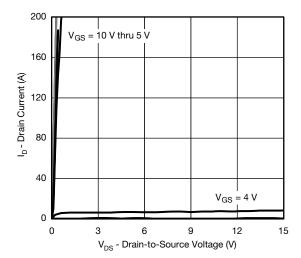
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static					•			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0$ , $I_D = 250 \mu A$		40	-	-	V	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		3.0	3.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
		$V_{GS} = 0 V$	V <sub>DS</sub> = 40 V	-	-	1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 40 V, T <sub>J</sub> = 125 °C	-	-	50	μА	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 40 V, T <sub>J</sub> = 175 °C	-	-	250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	120	-	-	Α	
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A	-	0.0014	0.0021		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C	-	-	0.0033	Ω	
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C	-	-	0.0040	-	
Forward Transconductanceb	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		-	166	-	S	
Dynamic <sup>b</sup>								
Input Capacitance	C <sub>iss</sub>			-	7030	8790		
Output Capacitance	Coss	$V_{GS} = 0 V$	$V_{GS} = 0 \text{ V}$ $V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		1180	1475	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			-	445	555		
Total Gate Charge <sup>c</sup>	Qg			-	180	270		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$V_{DS} = 10 \text{ V}, I_D = 16 \text{ A}$	-	44	-	nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			-	40	-		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		4.5	9.13	13.7	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	12	18		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 20 V, $R_L$ = 1 $\Omega$ $I_D \cong$ 20 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		-	5	8	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	248	370		
Fall Time <sup>c</sup>	t <sub>f</sub>			-	92	145		
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>	•			•	•		
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	480	Α	

#### Notes

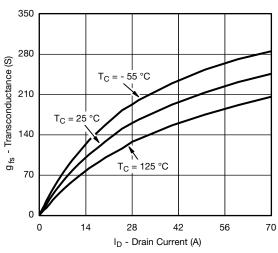
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

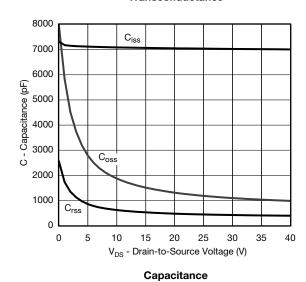
### TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)

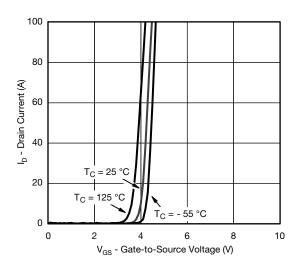


#### **Output Characteristics**

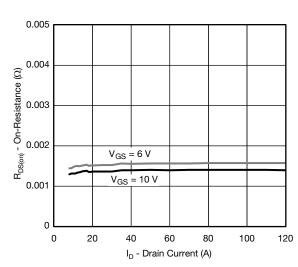


### Transconductance

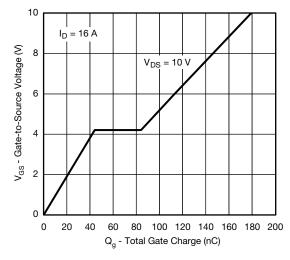




#### **Transfer Characteristics**



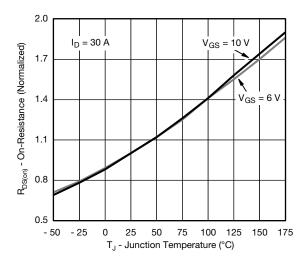
#### On-Resistance vs. Drain Current



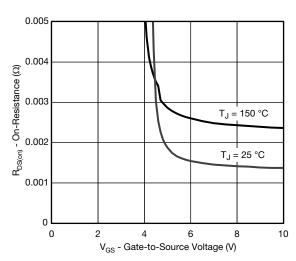
Gate Charge



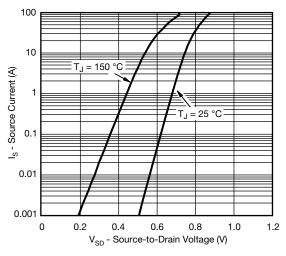
### TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



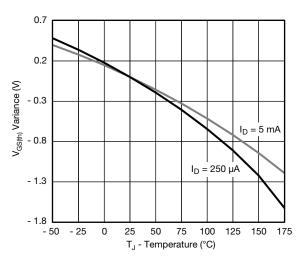
#### On-Resistance vs. Junction Temperature



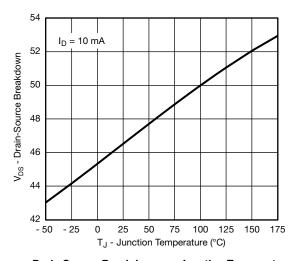
On-Resistance vs. Gate-to-Source Voltage



Source Drain Diode Forward Voltage



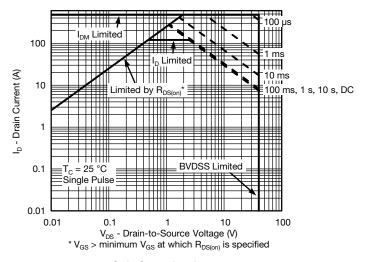
Threshold Voltage



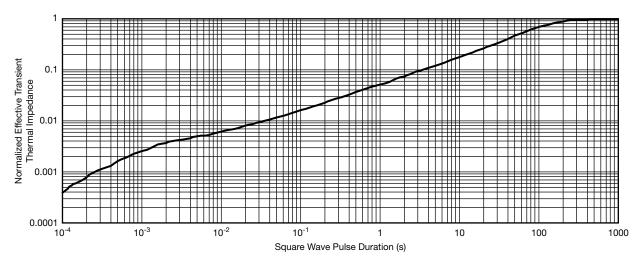
Drain Source Breakdown vs. Junction Temperature



### THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



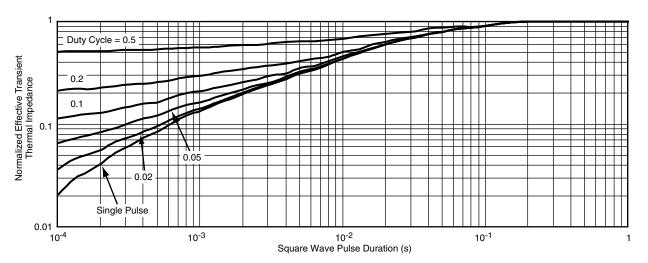
#### Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



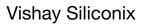
Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

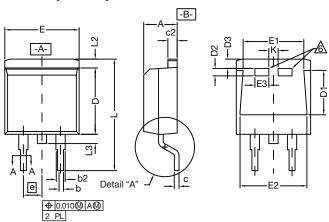
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

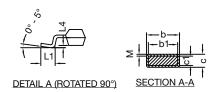
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### TO-263 (D<sup>2</sup>PAK): 3-LEAD





		INC	HES	MILLIN	METERS	
DIM.		MIN.	MAX.	MIN.	MAX.	
Α		0.160	0.190	4.064	4.826	
	b	0.020	0.039	0.508	0.990	
	b1	0.020	0.035	0.508	0.889	
	b2	0.045	0.055	1.143	1.397	
	Thin lead	0.013	0.018	0.330	0.457	
C*	Thick lead	0.023	0.028	0.584	0.711	
c1	Thin lead	0.013	0.017	0.330	0.431	
CI	Thick lead	0.023	0.027	0.584	0.685	
	c2	0.045	0.055	1.143	1.397	
	D	0.340	0.380	8.636	9.652	
D1		D1 0.220		5.588	6.096	
D2		0.038	0.042	0.965	1.067	
D3		0.045	0.055	1.143	1.397	
E		0.380	0.410	9.652	10.414	
	E1	0.245	-	6.223	-	
	E2	0.355	0.375	9.017 9.52		
	E3	0.072	0.078	1.829 1.98		
	е	0.100 BSC		2.54 BSC		
K		0.045	0.055	0.055 1.143 1.397		
	L	0.575	0.625	14.605	15.875	
L1		0.090	0.110	2.286	2.794	
L2		0.040	0.055	1.016	1.397	
L3		0.050	0.070	1.270	1.778	
L4		0.010 BSC		0.254 BSC		
М		-	0.002	-	0.050	
ECN: T10-0738-Rev. J, 03-Jan-11 DWG: 5843						

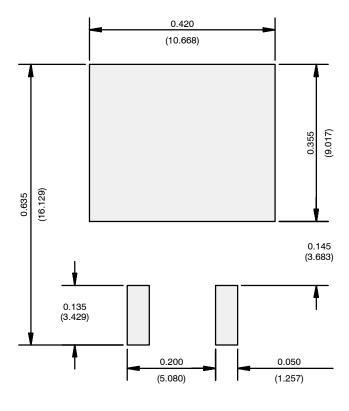
### Notes

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. \*: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.





### RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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