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RC4558 DUAL GENERAL-PURPOSE OPERATIONAL AMPLIFIER

SLOS073D - MARCH 1976 - REVISED SEPTEMBER 2004

- Continuous Short-Circuit Protection
- Wide Common-Mode and Differential Voltage Ranges
- No Frequency Compensation Required
- Low Power Consumption
- No Latch-Up
- Unity-Gain Bandwidth . . . 3 MHz Typ
- Gain and Phase Match Between Amplifiers
- Low Noise . . . 8 nV/√Hz Typ at 1 kHz

10UT [1 8] V_{CC}+ 1IN- [2 7] 20UT 1IN+ [3 6] 2INV_{CC}- [4 5] 2IN+

D, DGK, P, PS, OR PW PACKAGE

description/ordering information

The RC4558 device is a dual general-purpose operational amplifier, with each half electrically similar to the μ A741, except that offset null capability is not provided.

The high common-mode input voltage range and the absence of latch-up make this amplifier ideal for voltage-follower applications. The device is short-circuit protected, and the internal frequency compensation ensures stability without external components.

ORDERING INFORMATION

TA	PACKAGET		ORDERABLE PART NUMBER	TOP-SIDE MARKING	
	MSOP/VSSOP (DGK)	Reel of 2500	RC4558DGKR	YR_‡	
	PDIP (P)	Tube of 50	RC4558P	RC4558P	
	2010 (D)	Tube of 75	RC4558D	DOAFFO	
0°C to 70°C	SOIC (D)	Reel of 2500	RC4558DR	RC4558	
	SOP (PS)	Reel of 2000	RC4558PSR	R4558	
	TSSOP (PW)	Tube of 150	RC4558PW	R4558	
	1350P (PW)	Reel of 2000	RC4558PWR	K4000	
	MSOP/VSSOP (DGK)	Reel of 2500	RC4558IDGKR	YS_‡	
	PDIP (P)	Tube of 50	RC4558IP	RC4558IP	
-40°C to 85°C	2010 (D)	Tube of 75	RC4558ID	D.45501	
-40°C to 85°C	SOIC (D)	Reel of 2500	RC4558IDR	R4558I	
	TSSOD (DW)	Tube of 150	RC4558IPW	R4558I	
	TSSOP (PW)	Reel of 2000	RC4558IPWR	K40001	

[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



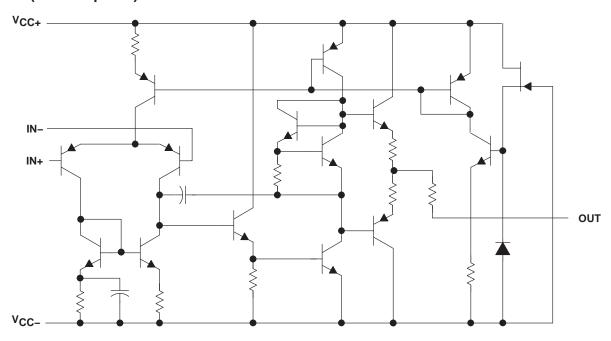
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[‡]The actual top-side marking has one additional character that designates the assembly/test site.

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schematic (each amplifier)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage (see Note 1): V _{CC+}		18 V
V _{CC}		–18 V
Differential input voltage, V _{ID} (see Note 2)		±30 V
Input voltage, V _I (any input, see Notes 1 and 3)		±15 V
Duration of output short circuit to ground, one amplifie	r at a time (see Note 4)	Unlimited
Package thermal impedance, θ _{JA} (see Notes 5 and 6)	: D package	97°C/W
3 71.	DGK package	172°C/W
	P package	85°C/W
	PS package	95°C/W
	PW package	149°C/W
Operating virtual junction temperature, T.I		150°C
Storage temperature range, T _{stq}		. −65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, unless otherwise noted, are with respect to the midpoint between V_{CC+} and V_{CC-}.
 - 2. Differential voltages are at IN+ with respect to IN-.
 - 3. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 15 V, whichever is less.
 - 4. Temperature and/or supply voltages must be limited to ensure that the dissipation rating is not exceeded.
 - 5. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
 - 6. The package thermal impedance is calculated in accordance with JESD 51-7.



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recommended operating conditions

			MIN	MAX	UNIT
V _{CC+}	Complements		5	15	V
VCC-	Supply voltage		-5	-15	V
т.	Operating free directory and the compositions	RC4558	0	70	00
TA	Operating free-air temperature RC4558I				°C

electrical characteristics at specified free-air temperature, V_{CC+} = 15 V, V_{CC-} = -15 V

	PARAMETER		TEST COND	ITIONS†	MIN	TYP	MAX	UNIT	
				25°C		0.5	6		
V _{IO}	Input offset voltage		$V_O = 0$	Full			7.5	mV	
				range			7.5		
				25°C		5	200		
liO	Input offset current		AO = 0	Full			300	nA	
			range						
				25°C		150	500		
I _{IB}	Input bias current		VO = 0	Full			800	nA	
				range					
VICR	Common-mode input voltage range			25°C	±12	±14		V	
			$R_L = 10 \text{ k}\Omega$	25°C	±12	±14			
Vом	Maximum output voltage swing		25°C	±10	±13		·		
Olvi	3	$R_L = 2 k\Omega$	Full	±10					
				range					
			$R_L \ge 2 k\Omega$,	25°C	20	300		V/mV	
AVD	Large-signal differential voltage amplification	$V_0 = \pm 10 \text{ V}$	Full	15					
B ₁	Unity-gain bandwith			range 25°C		3		MHz	
	Input resistance			25°C	0.3	5		MΩ	
r _i CMRR	'			25°C	70	90		dB	
CIVIRK	Common-mode rejection ratio		\/ \ \ \ \ \ \ \ \ \ \ \ \ \	25°C	70	90		иь	
ksvs	Supply-voltage sensitivity ($\Delta V_{IO}/\Delta V_{CC}$)		$V_{CC} = \pm 15 \text{ V}$ to $\pm 9 \text{ V}$	25°C		30	150	μV/V	
V _n	Equivalent input noise voltage (closed loop)		$A_{VD} = 100,$ $R_{S} = 100 \Omega,$ f = 1 kHz,	25°C		8		nV/√ Hz	
		BW = 1 Hz							
				25°C		2.5	5.6		
ICC	Supply current (both amplifiers)	V _O = 0, No load	T _A (min)		3	6.6	mA		
		No load	T _{A(max)}		2.3	5			
PD			V _O = 0, No load	25°C		75	170	mW	
	Total power dissipation	T _{A(min)}			90	200			
_	(both amplifiers)	140 loau	T _{A(max)}		70	150			
	Open lo		$R_S = 1 k\Omega$,			85			
V _{O1} /V _{O2}	Crosstalk attenuation	A _{VD} = 100	f = 10 kHz	25°C		105		dB	

[†] All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified. Full range is 0°C to 70°C for RC4558 and -40°C to 85°C for RC4558I.

operating characteristics, V_{CC+} = 15 V, V_{CC-} = -15 V, T_A = 25°C

PARAMETER			MIN	TYP	MAX	UNIT		
t _r	Rise time	V _I = 20 mV,	$R_L = 2 k\Omega$,	C _L = 100 pF		0.13		ns
	Overshoot	$V_{I} = 20 \text{ mV},$	$R_L = 2 k\Omega$,	C _L = 100 pF		5		%
SR	Slew rate at unity gain	V _I = 10 V,	$R_L = 2 k\Omega$,	C _L = 100 pF	1.1	1.7		V/µs









PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp (3)
RC4558D	ACTIVE	SOIC	D	8	75	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
RC4558DGKR	ACTIVE	MSOP	DGK	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
RC4558DR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4558ID	ACTIVE	SOIC	D	8	75	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
RC4558IDGKR	ACTIVE	MSOP	DGK	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1YEAR
RC4558IDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
RC4558IP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
RC4558IPW	ACTIVE	TSSOP	PW	8	150	Pb-Free (RoHS)	CU NIPDAU	Level-1-250C-UNLIM
RC4558IPWR	ACTIVE	TSSOP	PW	8	2000	Pb-Free (RoHS)	CU NIPDAU	Level-1-250C-UNLIM
RC4558P	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	Level-NC-NC-NC
RC4558PSLE	OBSOLETE	SO	PS	8		None	Call TI	Call TI
RC4558PSR	ACTIVE	SO	PS	8	2000	Pb-Free (RoHS)	CU NIPDAU	Level-2-260C-1 YEAR/ Level-1-235C-UNLIM
RC4558PW	ACTIVE	TSSOP	PW	8	150	Pb-Free (RoHS)	CU NIPDAU	Level-1-250C-UNLIM
RC4558PWLE	OBSOLETE	TSSOP	PW	8		None	Call TI	Call TI
RC4558PWR	ACTIVE	TSSOP	PW	8	2000	Pb-Free (RoHS)	CU NIPDAU	Level-1-250C-UNLIM
RC4558Y	OBSOLETE	XCEPT	Υ	0		None	Call TI	Call TI

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

None: Not yet available Lead (Pb-Free).

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⁽²⁾ Eco Plan - May not be currently available - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDECindustry standard classifications, and peak solder temperature.



PACKAGE OPTION ADDENDUM

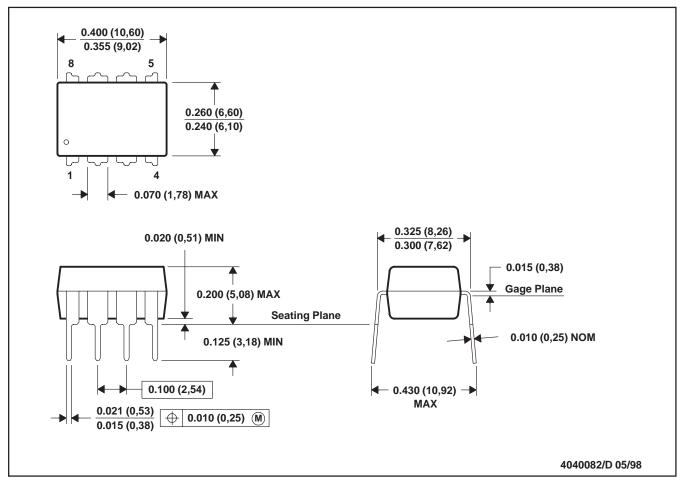
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P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE



NOTES: A. All linear dimensions are in inches (millimeters).

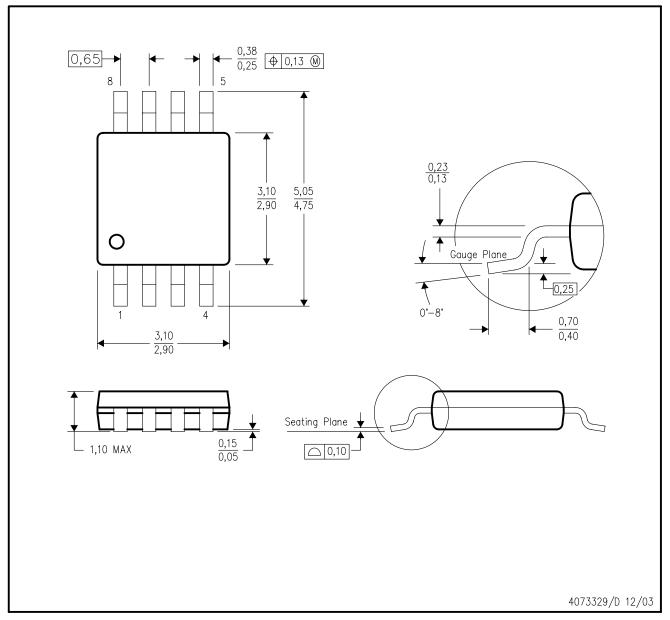
- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001

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DGK (S-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



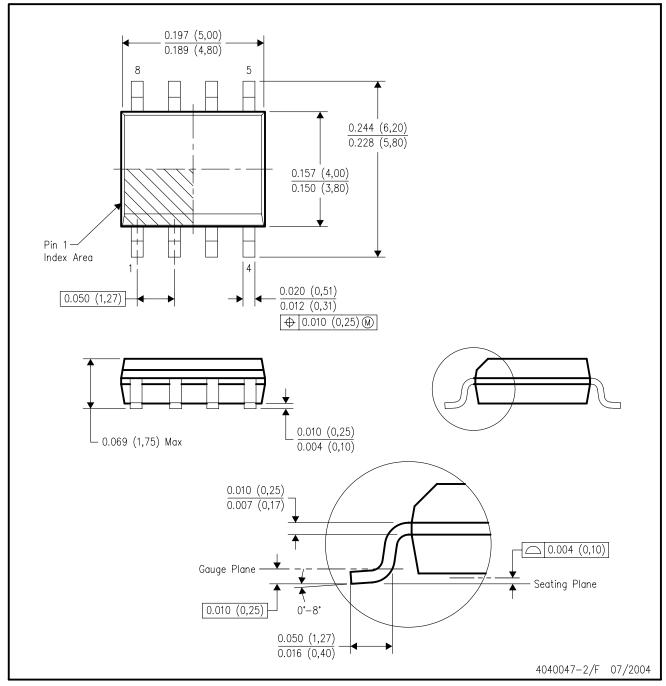
NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion.
- D. Falls within JEDEC MO-187 variation AA.



D (R-PDSO-G8)

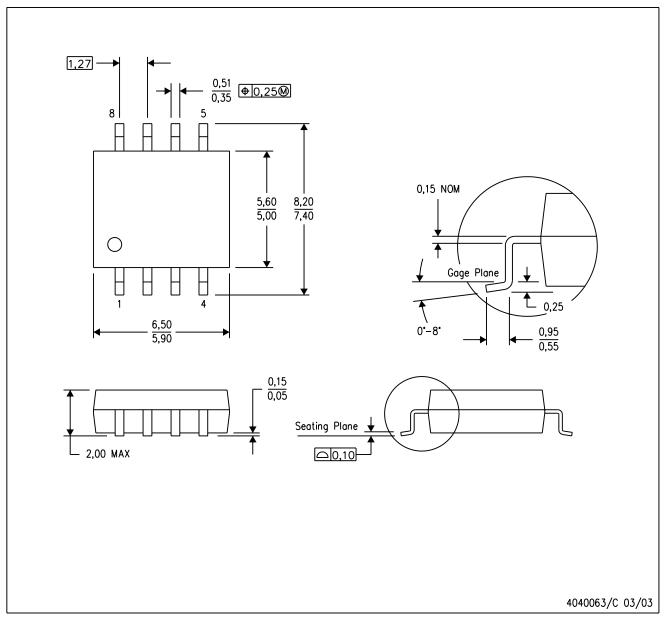
PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
- D. Falls within JEDEC MS-012 variation AA.





NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

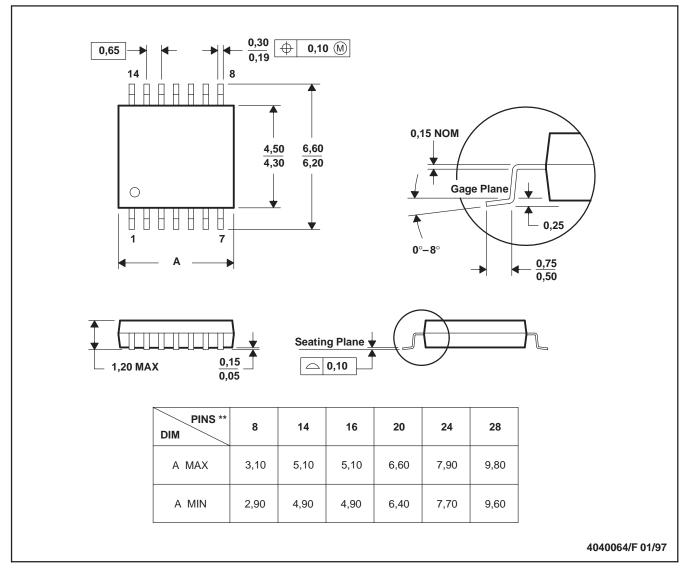
C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



PW (R-PDSO-G**)

14 PINS SHOWN

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153

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