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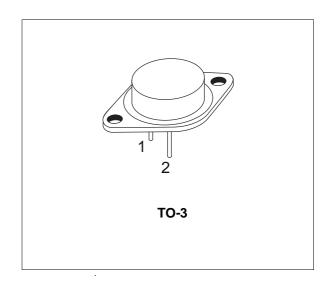


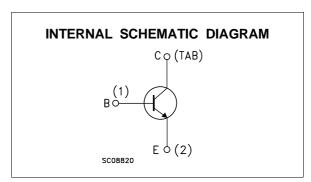
SILICON NPN POWER TRANSISTOR

 STMicroelectronics PREFERRED SALESTYPE

DESCRIPTION

The MJ802 is a silicon Epitaxial-Base power transistor mounted in Jedec TO-3 metal case. It is intended for general purpose power amplifier and switching applications.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CEO}	Collector-emitter Voltage (I _B = 0)	90	V
V _{CBO}	Collector-base Voltage (I _E = 0)	100	V
V _{ЕВО}	Emitter-Base Voltage (Ic = 0)	4	V
Ic	Collector Current	30	А
I _B	Base Current	7.5	А
Ptot	Total Dissipation at $T_c \le 25$ °C	200	W
T _{stg}	Storage Temperature	-65 to 200	°C
Tj	Max. Operating Junction Temperature	200	°C

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THERMAL DATA

ELECTRICAL CHARACTERISTICS ($T_{case} = 25$ $^{\circ}C$ unless otherwise specified)

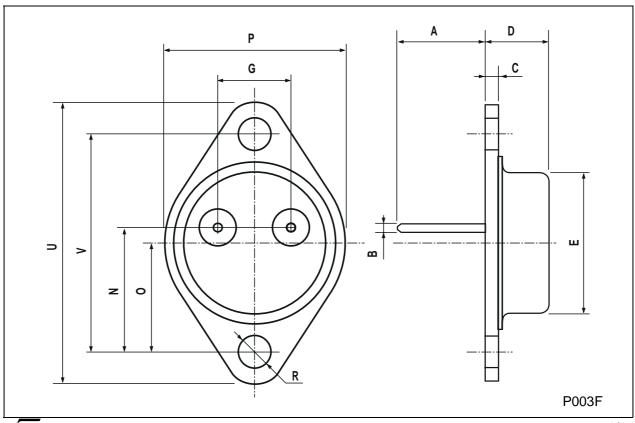
Symbol	Parameter	Test (Min.	Тур.	Max.	Unit	
Ісво	Collector Cut-off Current (I _E = 0)	V _{CB} = 100 V V _{CB} = 100 V	T _{case} = 150 °C			1 5	mA mA
I _{EBO}	Emitter Cut-off Current (I _C = 0)	$V_{EB} = 4 V$				1	mA
$V_{\text{CEO}(\text{sus})^*}$	Collector-Emitter Sustaining Voltage (I _B = 0)	I _C = 200 mA		90			V
$V_{CER(sus)^*}$	Collector-emitter Sustaining Voltage $(R_{BE} = 100 \Omega)$	I _C = 200 mA		100			V
$V_{CE(sat)^{*}}$	Collector-Emitter Saturation Voltage	I _C = 7.5 A	$I_B = 0.75 A$			0.8	V
V _{BE(sat)} *	Base-Emitter Saturation Voltage	I _C = 7.5 A	I _B = 0.75 A			1.3	V
V _{BE} *	Base-Emitter Voltage	I _C = 7.5 A	V _{CE} = 2 V			1.3	V
h _{FE} *	DC Current Gain	I _C = 7.5 A	V _{CE} = 2 V	25		100	
f⊤	Transition Frequency	I _C = 1 A f = 1 MHz	V _{CE} = 10 V	2			MHz

^{*} Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %

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TO-3 MECHANICAL DATA

DIM.	mm		inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	11.00		13.10	0.433		0.516
В	0.97		1.15	0.038		0.045
С	1.50		1.65	0.059		0.065
D	8.32		8.92	0.327		0.351
Е	19.00		20.00	0.748		0.787
G	10.70		11.10	0.421		0.437
N	16.50		17.20	0.649		0.677
Р	25.00		26.00	0.984		1.023
R	4.00		4.09	0.157		0.161
U	38.50		39.30	1.515		1.547
V	30.00		30.30	1.187		1.193



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