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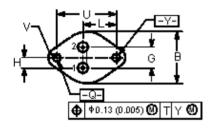
Complementary silicon power transistors.

The MJ15003 powerbaseTM power transistors designed for high power audio, disk head positioners and other linear applications.

Features:

- High safe operating area (100% tested) 5.0A at 50V.
- For low distortion complementary designs.
- High DC current gain h_{FE} = 25 (minimum) at I_C = 5A dc.
- Pb-free package.

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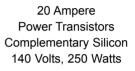
Style 1:

Pin 1. Base

2. Emitter Collector (Case)

Dimensions	Minimum	Maximum	
A	1.550 (39.37) Reference		
В	-	1.050 (26.67)	
С	0.250 (6.35)	0.335 (8.51)	
D	0.038 (0.97)	0.043 (1.09)	
E	0.055 (1.40)	0.070 (1.77)	
G	0.430 (10.92) BSC		
Н	0.215 (5.46) BSC		
к	0.440 (11.18) 0.480 (12.19		
L	0.665 (16.89) BSC		
N	-	0.830 (21.08)	
Q	0.151 (3.84) 0.165 (4.19)		
U	1.187 (30.15) BSC		
V	0.131 (3.33)	0.188 (4.77)	

Dimensions : Inches (Millimetres)





(TO-3) Case 1-07 Style 1



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Maximum Ratings (Note 1)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	140	
Collector-Base Voltage	V _{CBO}	140	V dc
Emitter-Base Voltage	V _{EBO}	5	
Collector Current - Continuous	۱ _C	20	A dc
Base Current - Continuous	Ι _Β	5	Auc
Emitter Current - Continuous	Ι _Ε	25	
Total Device Dissipation at T _C = 25°C Derate above 25°C	P _D	250 1.43	W W/°C
Operating and Storage Junction Temperature Range	T _{J,} T _{stg}	-65 to +200	℃

Thermal Characteristics

Characteristics	Symbol	Maximum	Unit
Thermal Resistance, Junction-to-Case	$R_{ extsf{ heta}JC}$	0.70	°C/W
Maximum Lead Temperature for Soldering Purpose 1/6 inches from Case for ≤10 Seconds	TL	265	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

Electrical Characteristics (T_c = 25°C unless otherwise noted)

Characteristic	Symbol	Minimum	Maximum	Unit
Off Characteristics				
Collector-Emitter Sustaining Voltage (Note 1) ($I_c = 200$ mA dc, $I_B = 0$)	V _{EO (sus)}	140	-	V dc
Collector Cut off Current ($V_{CE} = 140V \text{ dc}, V_{BE \text{ (off)}} = 1.5V \text{ dc}$) ($V_{CE} = 140V \text{ dc}, V_{BE \text{ (off)}} = 1.5V \text{ dc}, T_C = 150^{\circ}\text{C}$)	I _{CEX}	-	100 2	μA dc mA dc
Collector Cut off Current (V _{CE} = 140V dc, I _B = 0)	I _{CEO}	-	250	A
Emitter Cut off Current ($V_{EB} = 5V \text{ dc } I_C = 0$)	I _{EBO}	-	100	μA dc
Second Breakdown				
Second Breakdown Collector Current with Base Forward Biased ($V_{CE} = 50V \text{ dc}, t = 1s \text{ (non repetitive)}$) ($V_{CE} = 100V \text{ dc}, t = 1s \text{ (non repetitive)}$)	I _{S/b}	5.0 1.0	-	A dc

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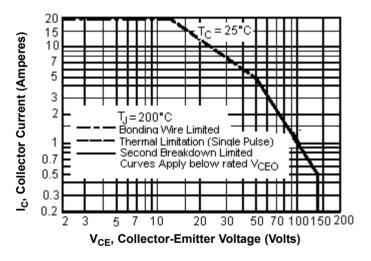


Electrical Characteristics (T_c = 25°C unless otherwise noted)

Characteristic	Symbol	Minimum	Maximum	Unit
On Characteristic				
DC Current Gain (I_C = 5A dc, V_{CE} = 2V dc)	h _{FE}	25	150	-
Collector-Emitter Saturation Voltage $(I_{C} = 5A dc, I_{B} = 0.5A dc)$	V _{CE (sat)}	-	1.0	- V dc
Base-Emitter On Voltage ($I_C = 5A dc, V_{CE} = 2V dc$)	V _{BE (on)}	-	2.0	
Dynamic Characteristics			1	I
Current-Gain - Bandwidth Product (I _C = 0.5A dc, V _{CE} = 10V dc, f _{test} = 0.5MHz)	f _T	2.0	-	MHz
Output Capacitance (V_{CB} = 10V dc, I _E = 0, f _{test} = 1MHz)	C _{ob}	-	1000	pF

1. Pulse Test : Pulse Width = $300\mu s$, Duty Cycle $\leq 2\%$.

Active - Region Safe Operating Area



Part Number Table

Description	Part Number
Transistor, NPN, TO-3	MJ15003

There are two limitation on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than curves indicate. The data is based on $T_{J (PK)} = 200^{\circ}C$; T_C is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

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