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Radxa ROCK 5B

schematic

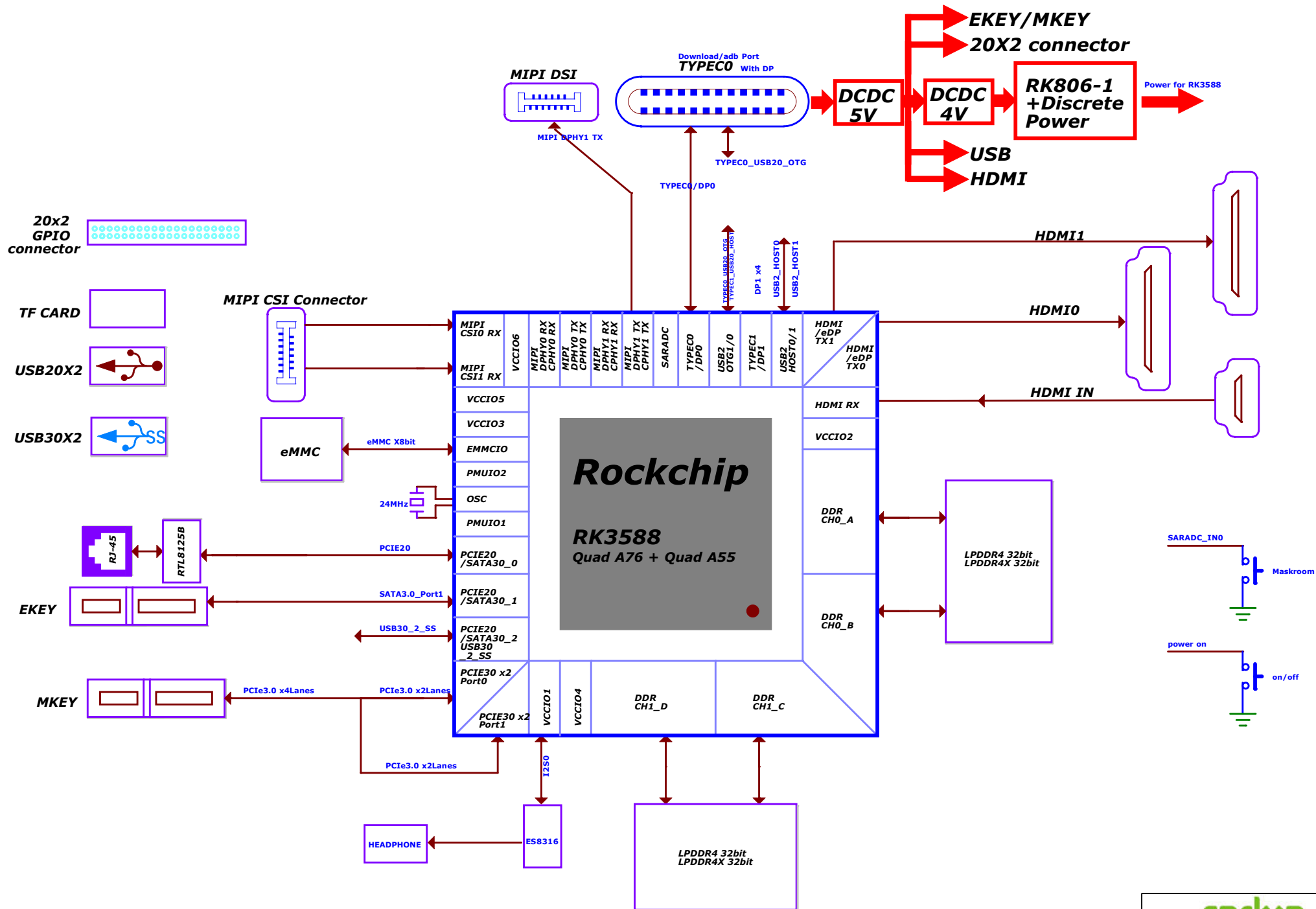
V1.45

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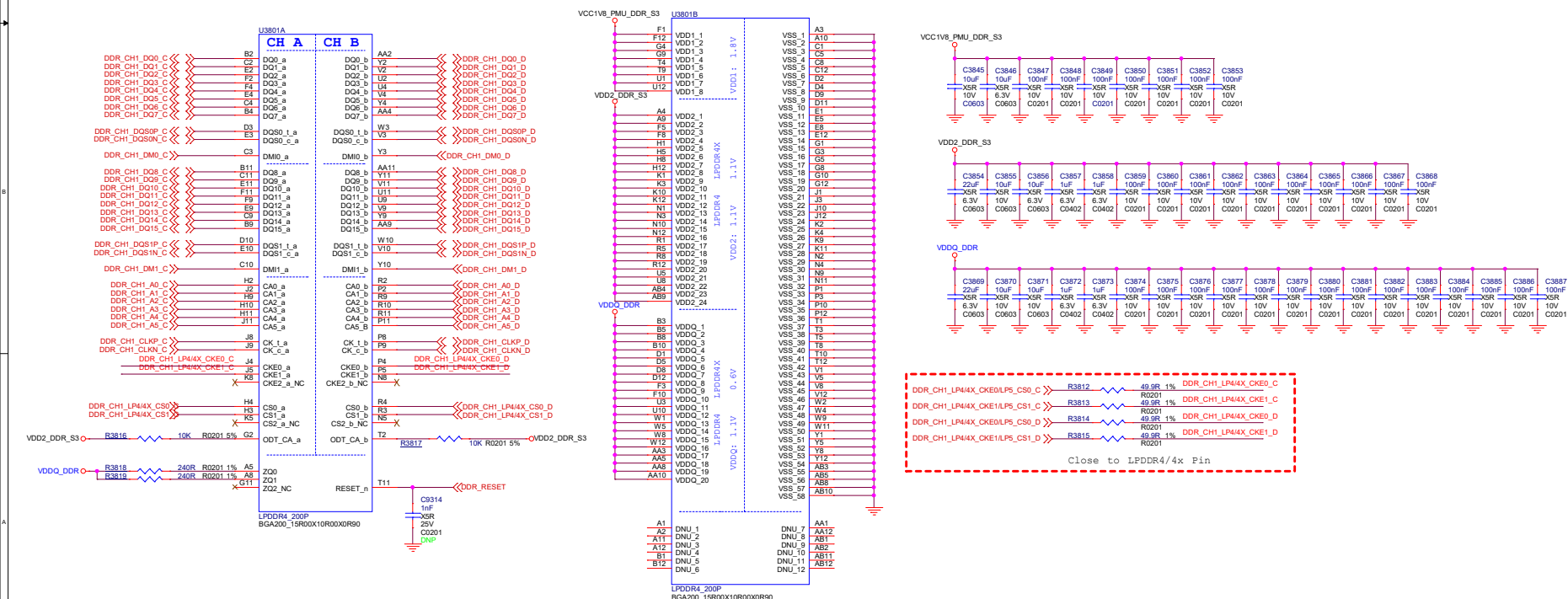
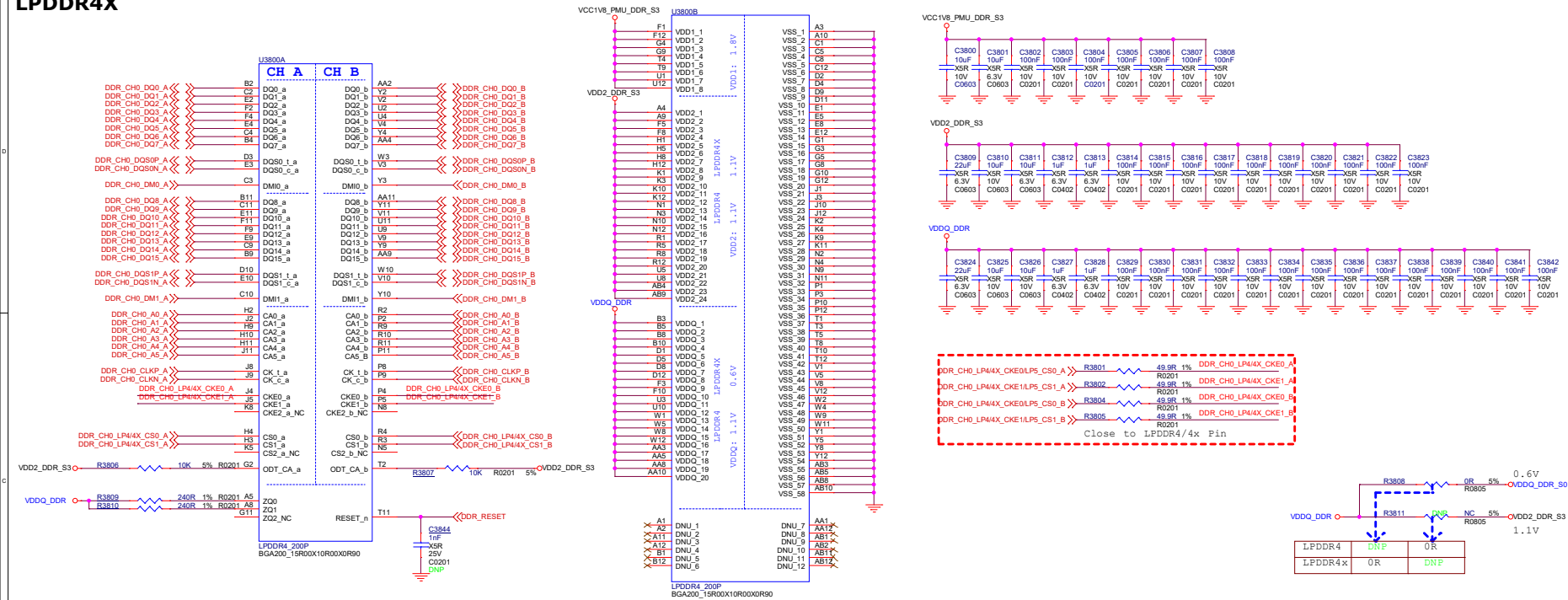
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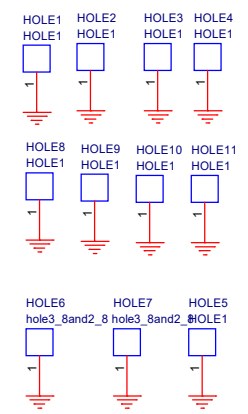
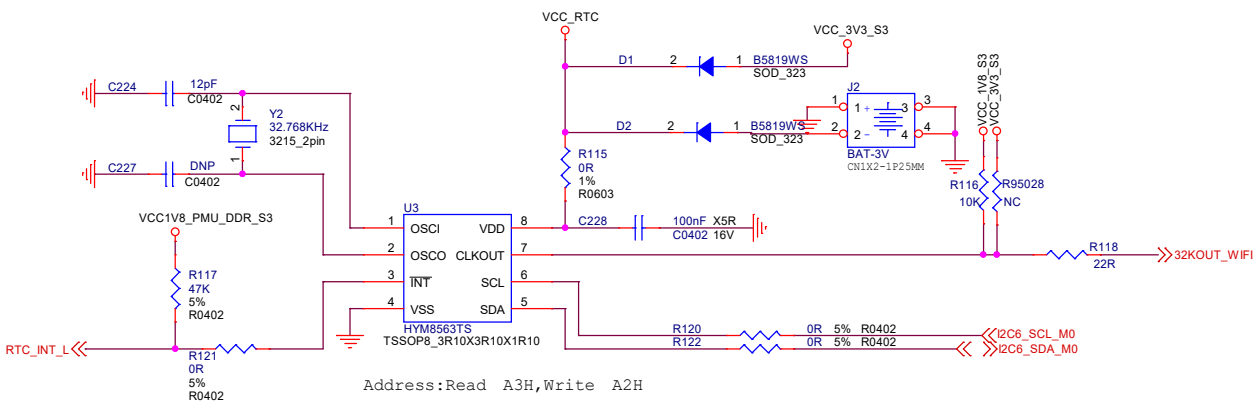
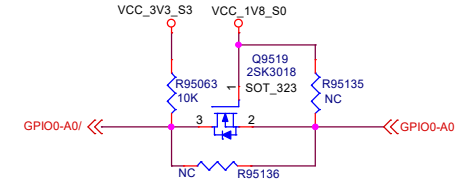
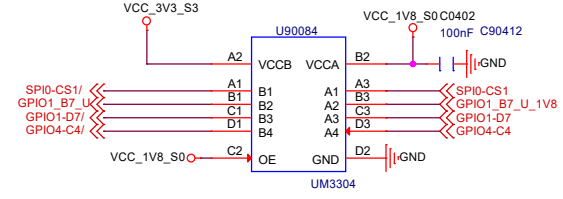
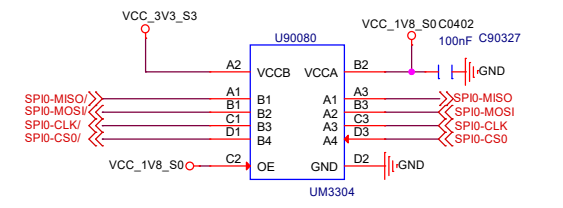
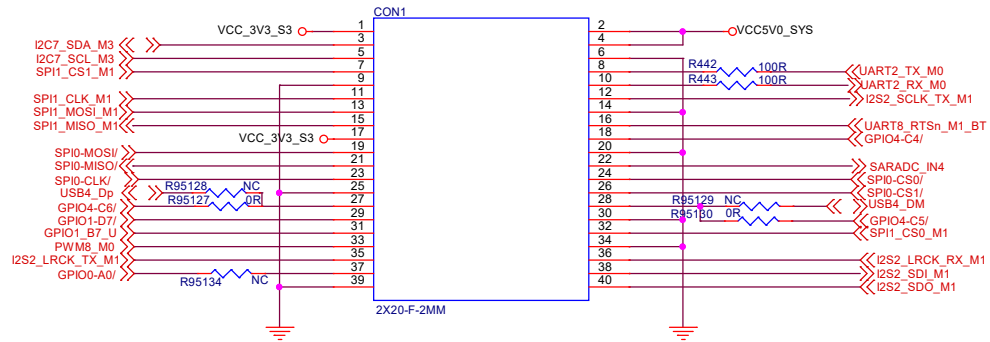
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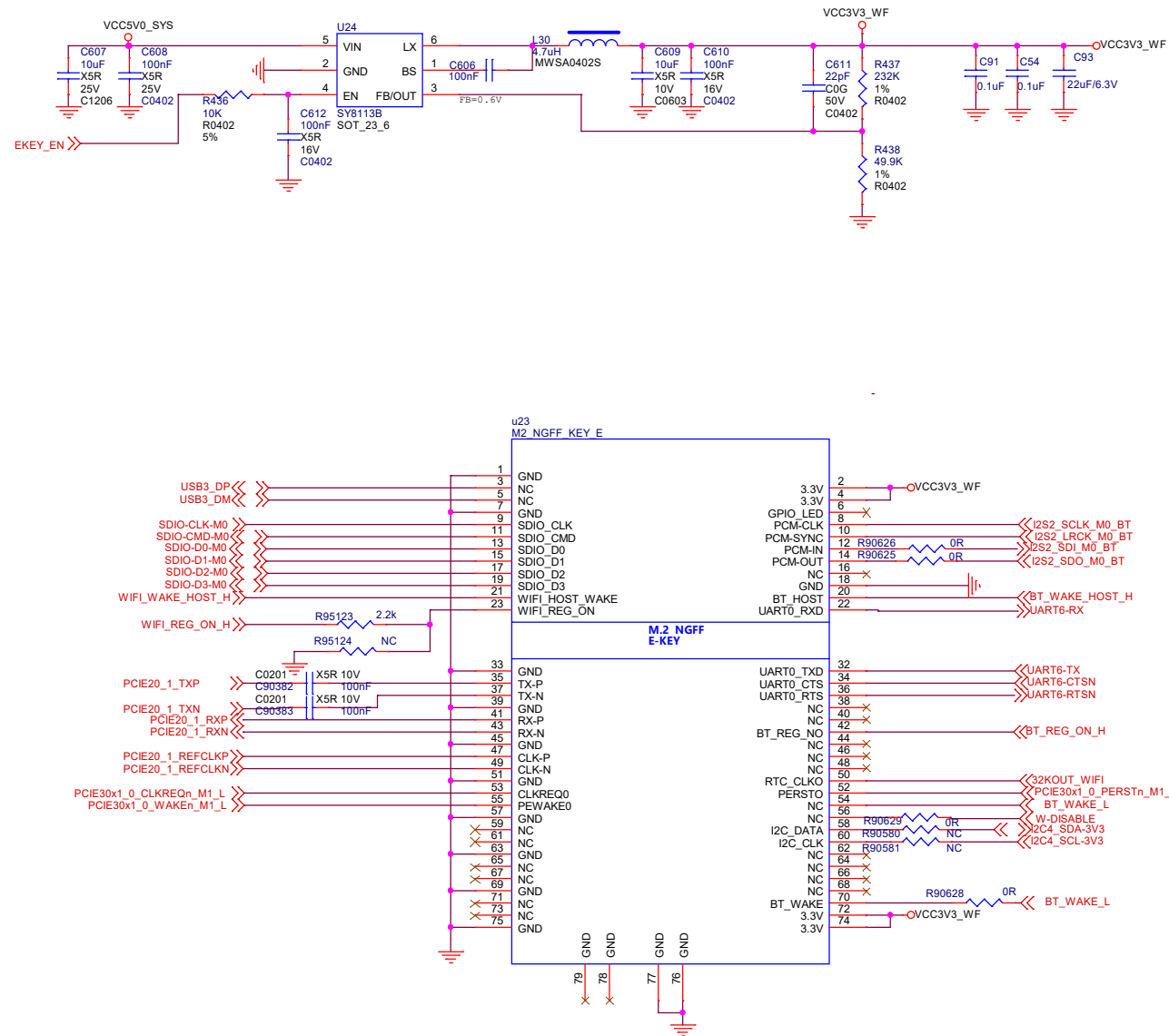
LPDDR4X



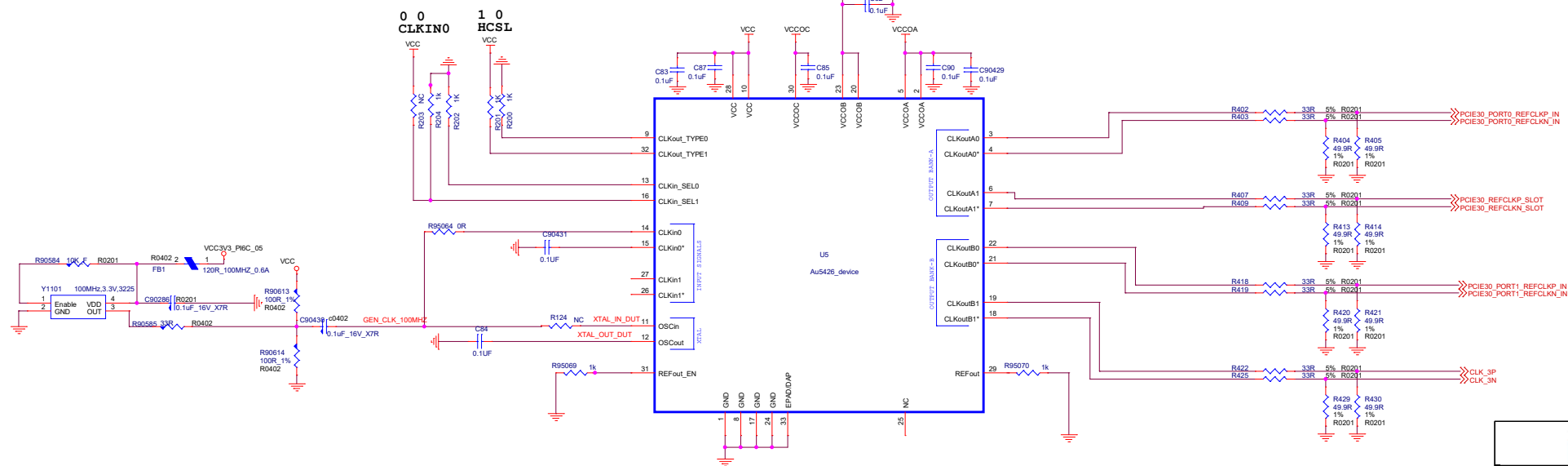
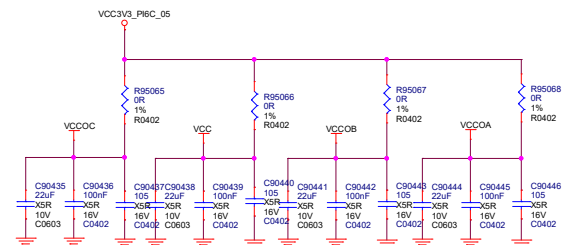
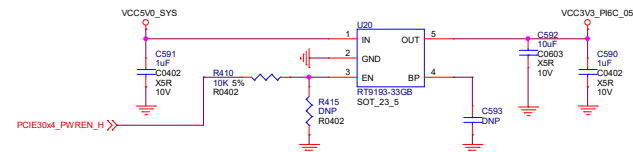
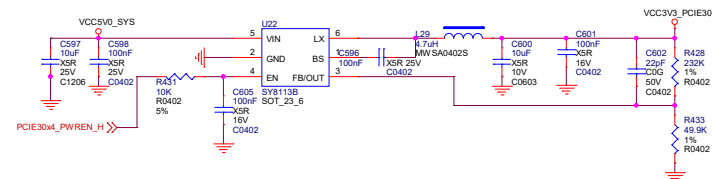
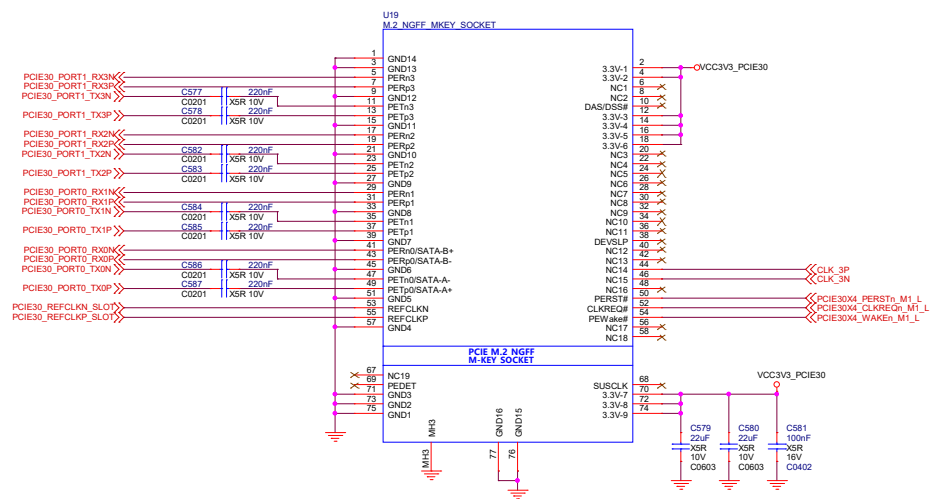
CONNECT



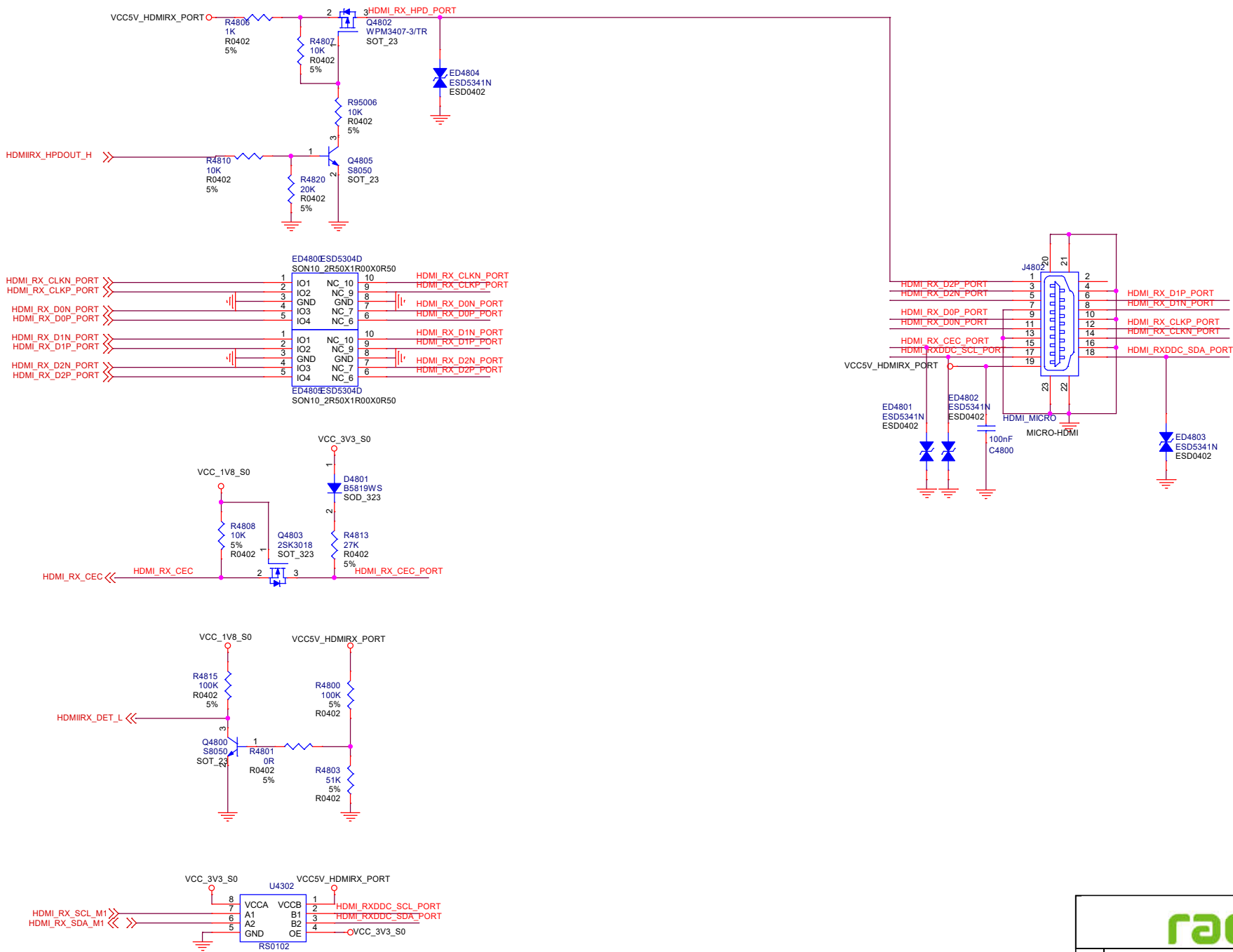
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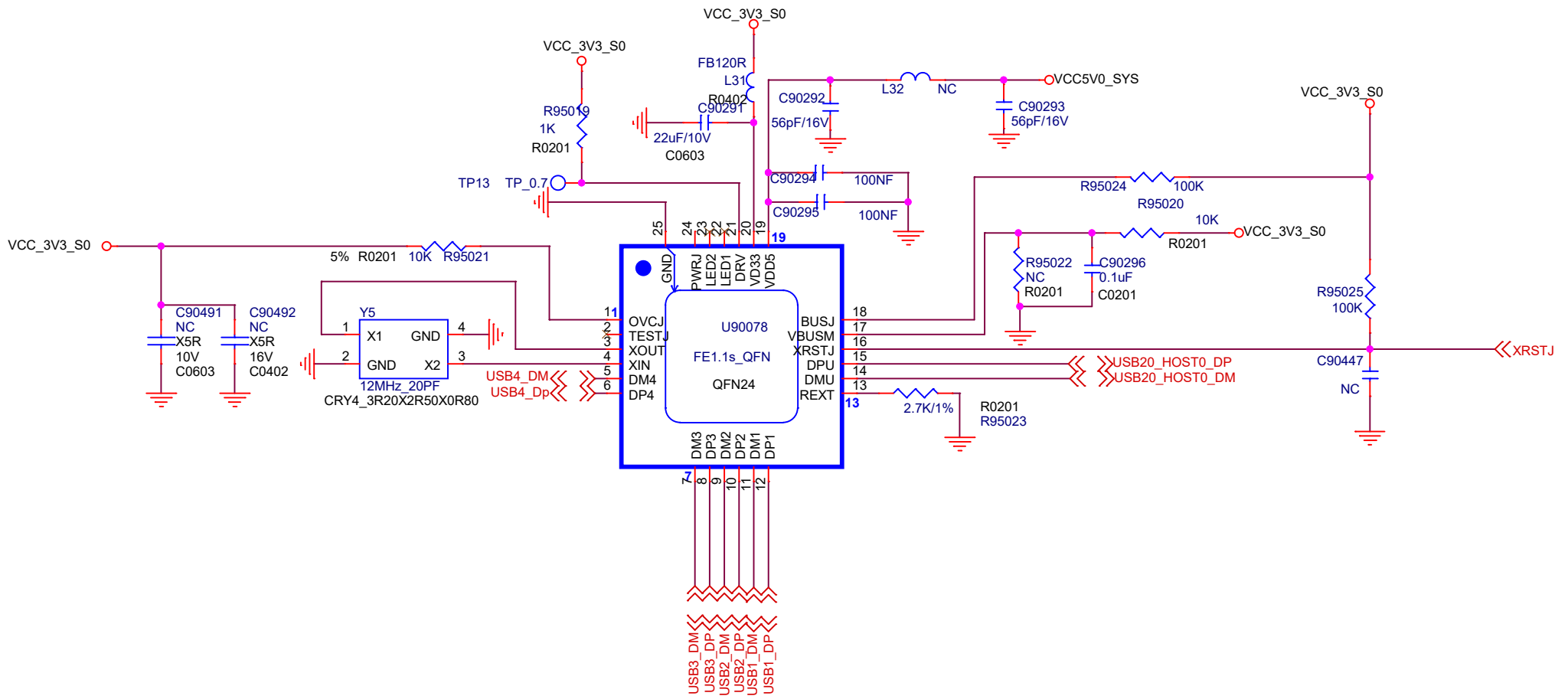
PCIe3.0 x 4 Slot



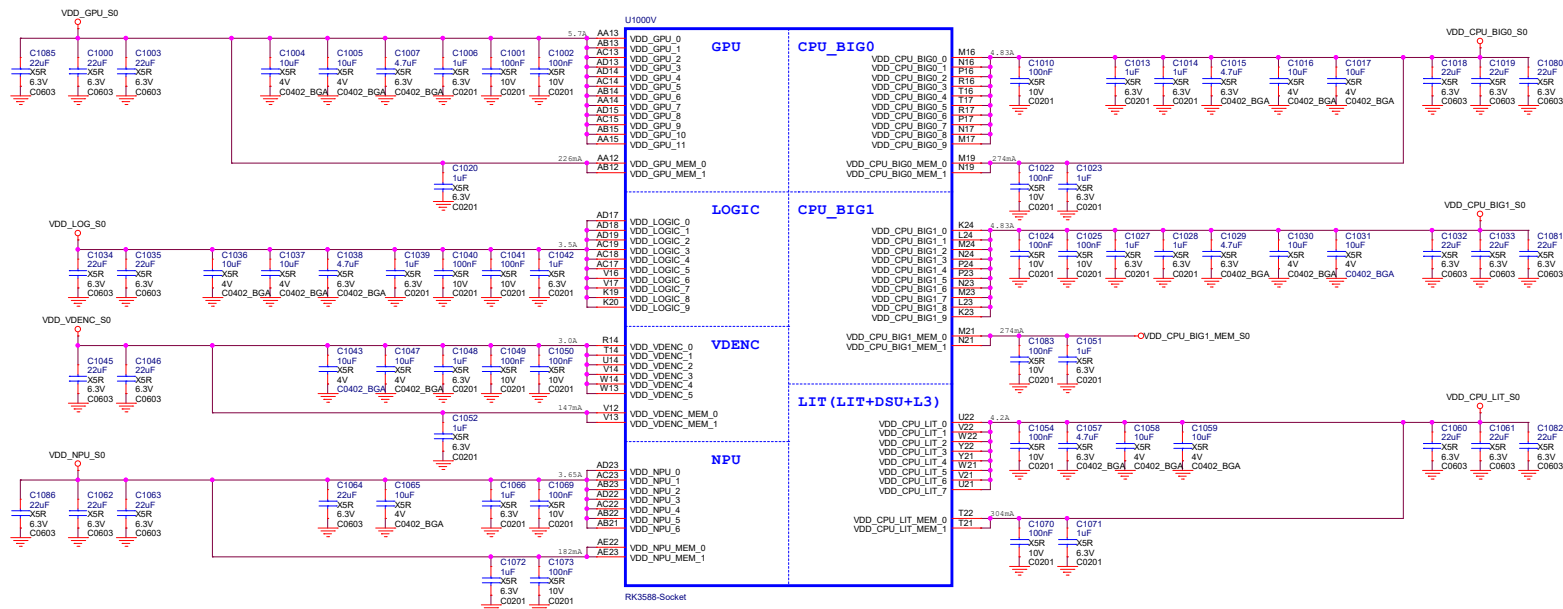
HDMI2.0 RX



USB2HUB



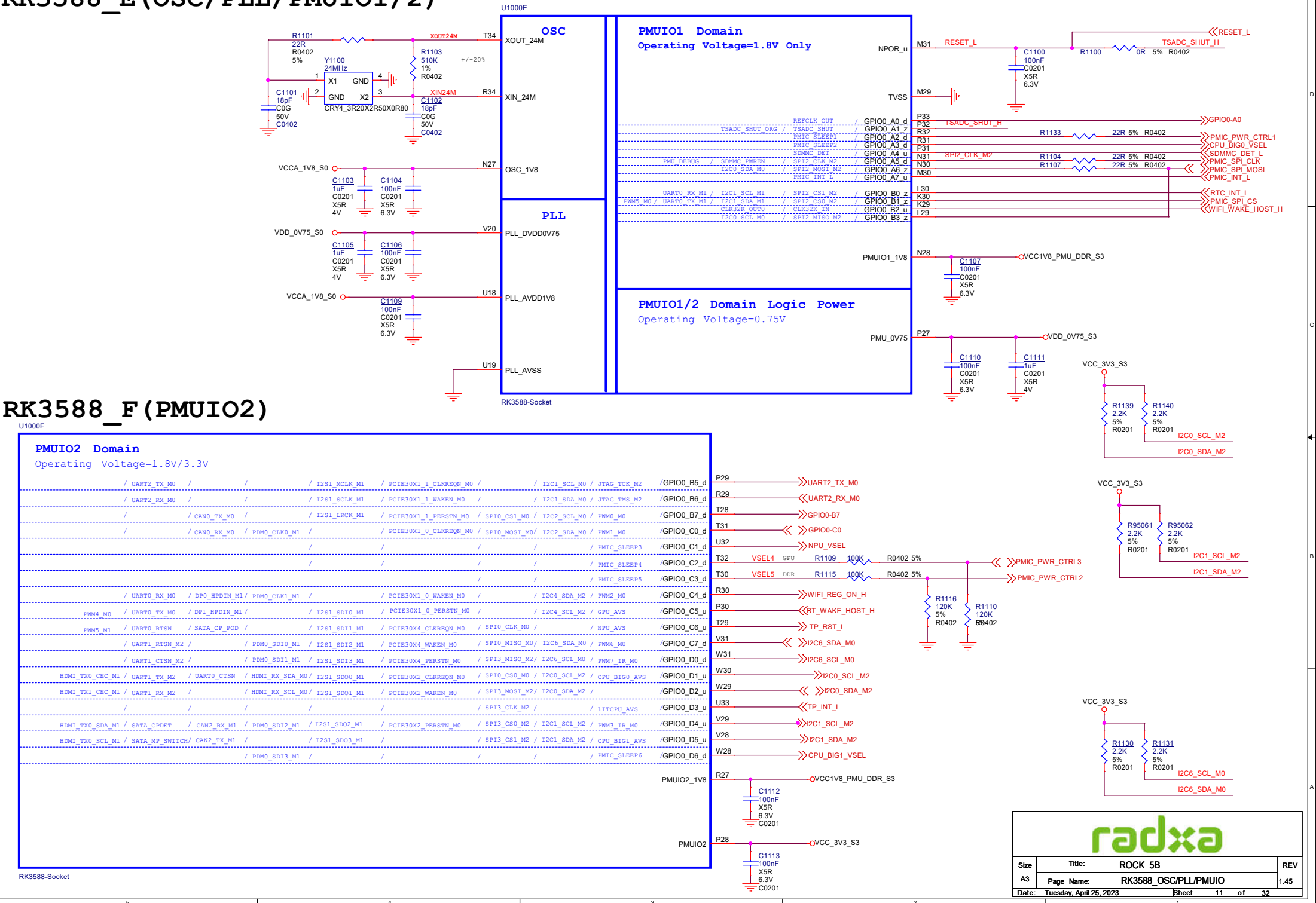
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U1000Z			U1000X			U1000W			U1000Y		
H28	AVSS_1	AH12	L3	VSS_107	R19	A1	VSS_1	F15	W3	VSS_213	AB18
H27	AVSS_2	AVSS_53	L8	VSS_108	R21	A14	VSS_2	VSS_55	W7	VSS_214	AB19
H26	AVSS_3	AVSS_54	L18	VSS_109	R22	A34	VSS_3	VSS_56	W9	VSS_215	AB20
H25	AVSS_4	AVSS_55	L28	VSS_110	R23	A35	VSS_4	VSS_57	W10	VSS_216	AB21
H24	AVSS_5	AVSS_56	L31	VSS_111	R24	B19	VSS_5	VSS_58	W11	VSS_217	AB22
H23	AVSS_6	AVSS_57	L32	VSS_112	R25	B20	VSS_6	VSS_59	W12	VSS_218	AB23
K31	AVSS_7	AVSS_58	L35	VSS_113	R26	B21	VSS_7	VSS_60	W13	VSS_219	AB24
K30	AVSS_8	AVSS_59	L36	VSS_114	R27	B22	VSS_8	VSS_61	W14	VSS_220	AB25
K29	AVSS_9	AVSS_60	L37	VSS_115	R28	B23	VSS_9	VSS_62	W15	VSS_221	AB26
L31	AVSS_10	AVSS_61	L38	VSS_116	R29	C1	VSS_10	VSS_63	W16	VSS_222	AB27
L30	AVSS_11	AVSS_62	L39	VSS_117	R30	C2	VSS_11	VSS_64	W17	VSS_223	AB28
M32	AVSS_12	AVSS_63	L40	VSS_118	R31	C3	VSS_12	VSS_65	W18	VSS_224	AB29
M31	AVSS_13	AVSS_64	L41	VSS_119	R32	C4	VSS_13	VSS_66	W19	VSS_225	AB30
A48	AVSS_14	AVSS_65	L42	VSS_120	R33	C5	VSS_14	VSS_67	W20	VSS_226	AB31
AB7	AVSS_15	AVSS_66	L43	VSS_121	R34	C6	VSS_15	VSS_68	W21	VSS_227	AB32
AB6	AVSS_16	AVSS_67	L44	VSS_122	R35	C7	VSS_16	VSS_69	W22	VSS_228	AB33
AB5	AVSS_17	AVSS_68	L45	VSS_123	R36	C8	VSS_17	VSS_70	W23	VSS_229	AB34
AB4	AVSS_18	AVSS_69	L46	VSS_124	R37	C9	VSS_18	VSS_71	W24	VSS_230	AB35
AB3	AVSS_19	AVSS_70	L47	VSS_125	R38	C10	VSS_19	VSS_72	W25	VSS_231	AB36
AB2	AVSS_20	AVSS_71	L48	VSS_126	R39	C11	VSS_20	VSS_73	W26	VSS_232	AB37
AB1	AVSS_21	AVSS_72	L49	VSS_127	R40	C12	VSS_21	VSS_74	W27	VSS_233	AB38
AC10	AVSS_22	AVSS_73	L50	VSS_128	R41	C13	VSS_22	VSS_75	W28	VSS_234	AB39
AD5	AVSS_23	AVSS_74	L51	VSS_129	R42	C14	VSS_23	VSS_76	W29	VSS_235	AB40
AD6	AVSS_24	AVSS_75	L52	VSS_130	R43	C15	VSS_24	VSS_77	W30	VSS_236	AB41
AD7	AVSS_25	AVSS_76	L53	VSS_131	R44	C16	VSS_25	VSS_78	W31	VSS_237	AB42
AE5	AVSS_26	AVSS_77	L54	VSS_132	R45	C17	VSS_26	VSS_79	W32	VSS_238	AB43
AE6	AVSS_27	AVSS_78	L55	VSS_133	R46	C18	VSS_27	VSS_80	W33	VSS_239	AB44
AE7	AVSS_28	AVSS_79	L56	VSS_134	R47	C19	VSS_28	VSS_81	W34	VSS_240	AB45
AE8	AVSS_29	AVSS_80	L57	VSS_135	R48	C20	VSS_29	VSS_82	W35	VSS_241	AB46
AF7	AVSS_30	AVSS_81	L58	VSS_136	R49	C21	VSS_30	VSS_83	W36	VSS_242	AB47
AF8	AVSS_31	AVSS_82	L59	VSS_137	R50	C22	VSS_31	VSS_84	W37	VSS_243	AB48
AF9	AVSS_32	AVSS_83	L60	VSS_138	R51	C23	VSS_32	VSS_85	W38	VSS_244	AB49
AF10	AVSS_33	AVSS_84	L61	VSS_139	R52	C24	VSS_33	VSS_86	W39	VSS_245	AB50
AF11	AVSS_34	AVSS_85	L62	VSS_140	R53	C25	VSS_34	VSS_87	W40	VSS_246	AB51
AF12	AVSS_35	AVSS_86	L63	VSS_141	R54	C26	VSS_35	VSS_88	W41	VSS_247	AB52
AF13	AVSS_36	AVSS_87	L64	VSS_142	R55	C27	VSS_36	VSS_89	W42	VSS_248	AB53
AF14	AVSS_37	AVSS_88	L65	VSS_143	R56	C28	VSS_37	VSS_90	W43	VSS_249	AB54
AF15	AVSS_38	AVSS_89	L66	VSS_144	R57	C29	VSS_38	VSS_91	W44	VSS_250	AB55
AF16	AVSS_39	AVSS_90	L67	VSS_145	R58	C30	VSS_39	VSS_92	W45	VSS_251	AB56
AF17	AVSS_40	AVSS_91	L68	VSS_146	R59	C31	VSS_40	VSS_93	W46	VSS_252	AB57
AF18	AVSS_41	AVSS_92	L69	VSS_147	R60	C32	VSS_41	VSS_94	W47	VSS_253	AB58
AF19	AVSS_42	AVSS_93	L70	VSS_148	R61	C33	VSS_42	VSS_95	W48	VSS_254	AB59
AF20	AVSS_43	AVSS_94	L71	VSS_149	R62	C34	VSS_43	VSS_96	W49	VSS_255	AB60
AF21	AVSS_44	AVSS_95	L72	VSS_150	R63	C35	VSS_44	VSS_97	W50	VSS_256	AB61
AG1	AVSS_45	AVSS_96	L73	VSS_151	R64	C36	VSS_45	VSS_98	W51	VSS_257	AB62
AG2	AVSS_46	AVSS_97	L74	VSS_152	R65	C37	VSS_46	VSS_99	W52	VSS_258	AB63
AG3	AVSS_47	AVSS_98	L75	VSS_153	R66	C38	VSS_47	VSS_100	W53	VSS_259	AB64
AG4	AVSS_48	AVSS_99	L76	VSS_154	R67	C39	VSS_48	VSS_101	W54	VSS_260	AB65
AG5	AVSS_49	AVSS_100	L77	VSS_155	R68	C40	VSS_49	VSS_102	W55	VSS_261	AB66
AG6	AVSS_50	AVSS_101	L78	VSS_156	R69	C41	VSS_50	VSS_103	W56	VSS_262	AB67
AG7	AVSS_51		L79	VSS_157	R70	C42	VSS_51	VSS_104	W57	VSS_263	AB68
AG8			L80	VSS_158	R71	C43	VSS_52	VSS_105	W58	VSS_264	AB69
AG9			L81	VSS_159	R72	C44	VSS_53	VSS_106	W59	VSS_265	AB70

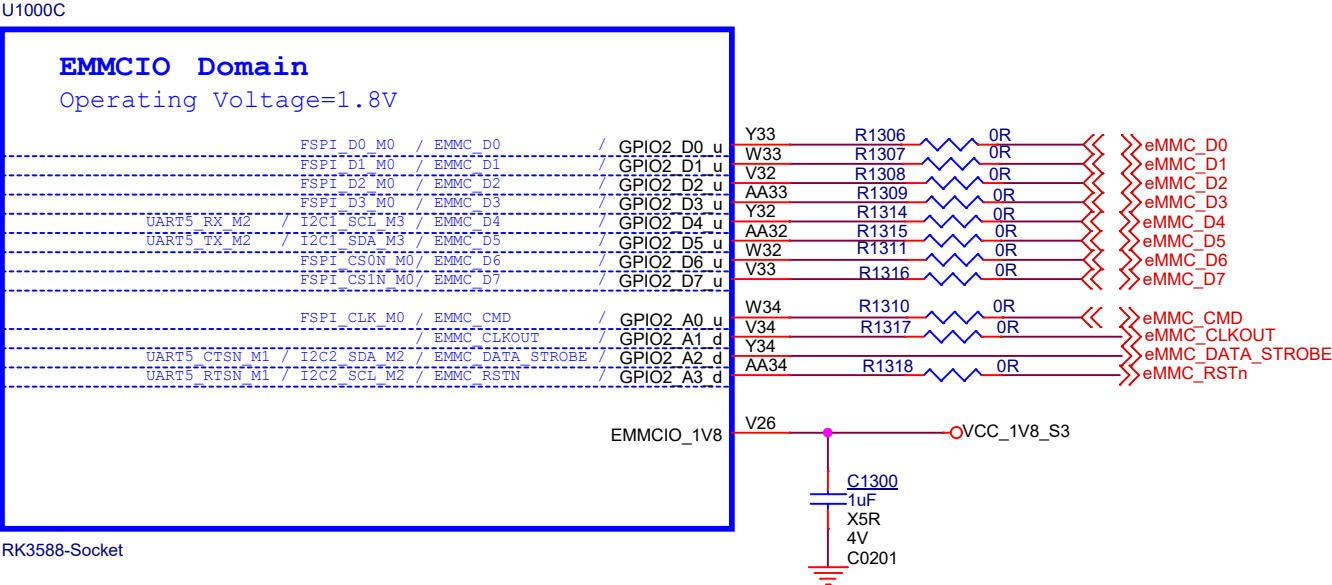
RK3588_E (OSC/PLL/PMUIO1/2)

RK3588_F (PMUIO2)

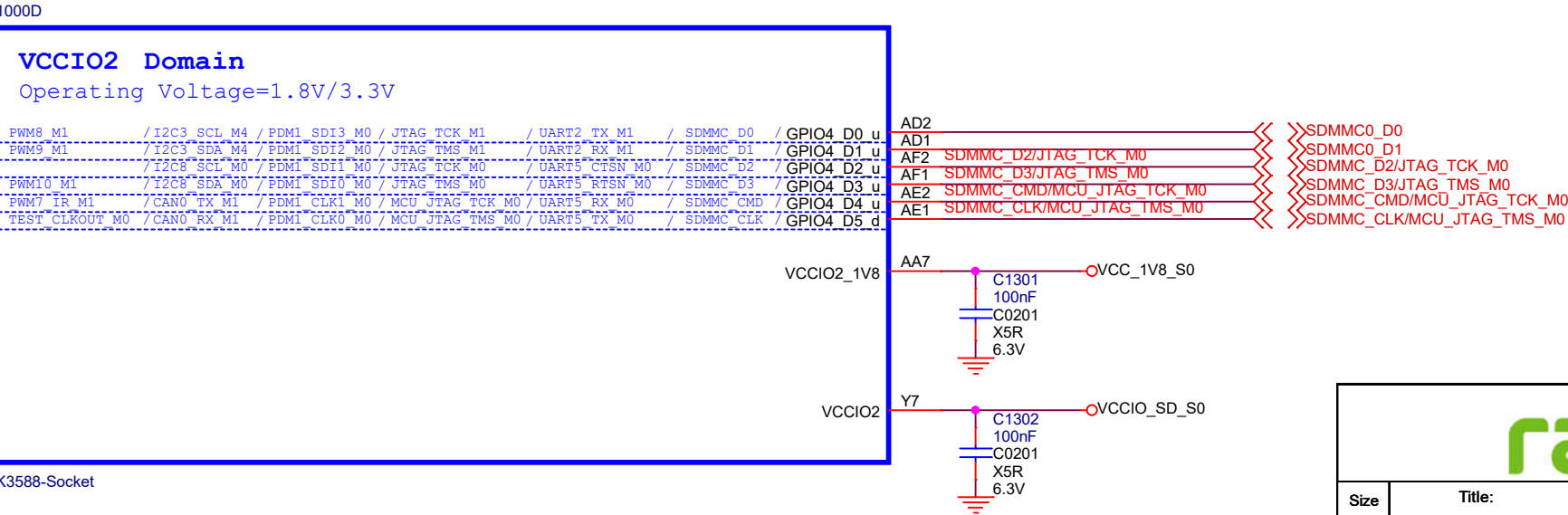




RK3588_C (EMMCIO Domain)

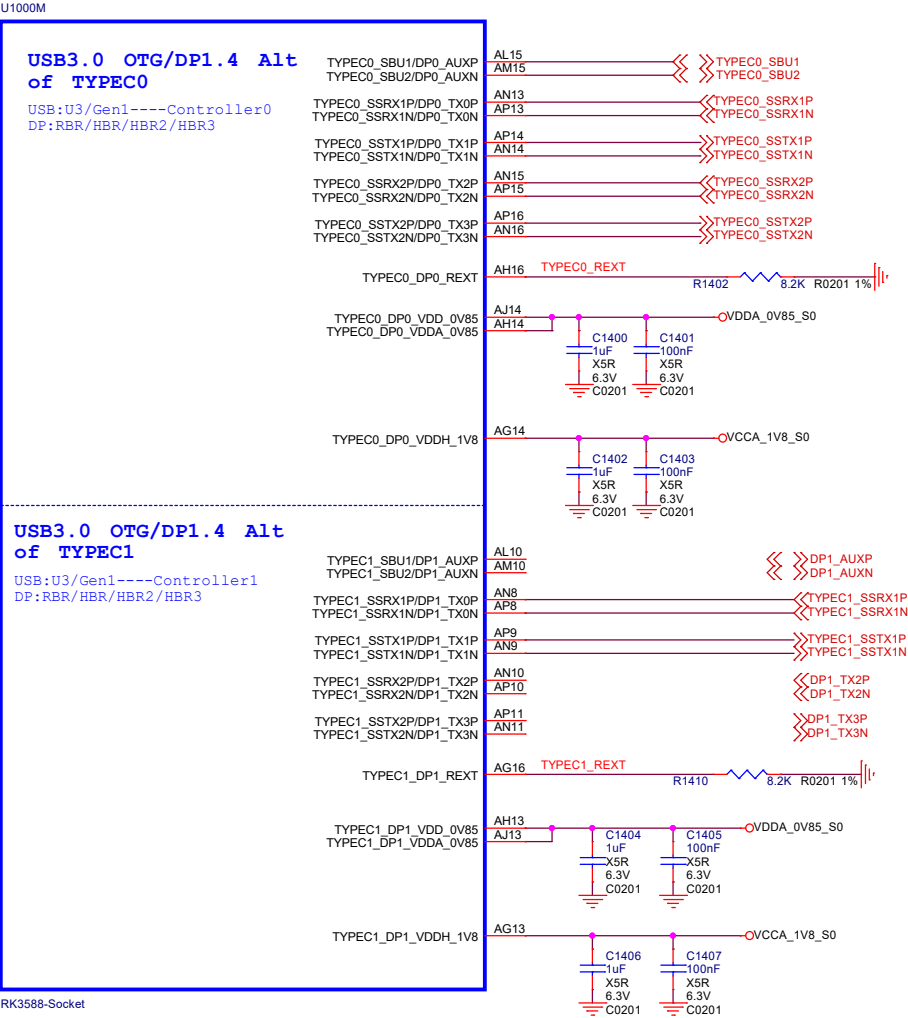


RK3588_D (VCCIO2 Domain)

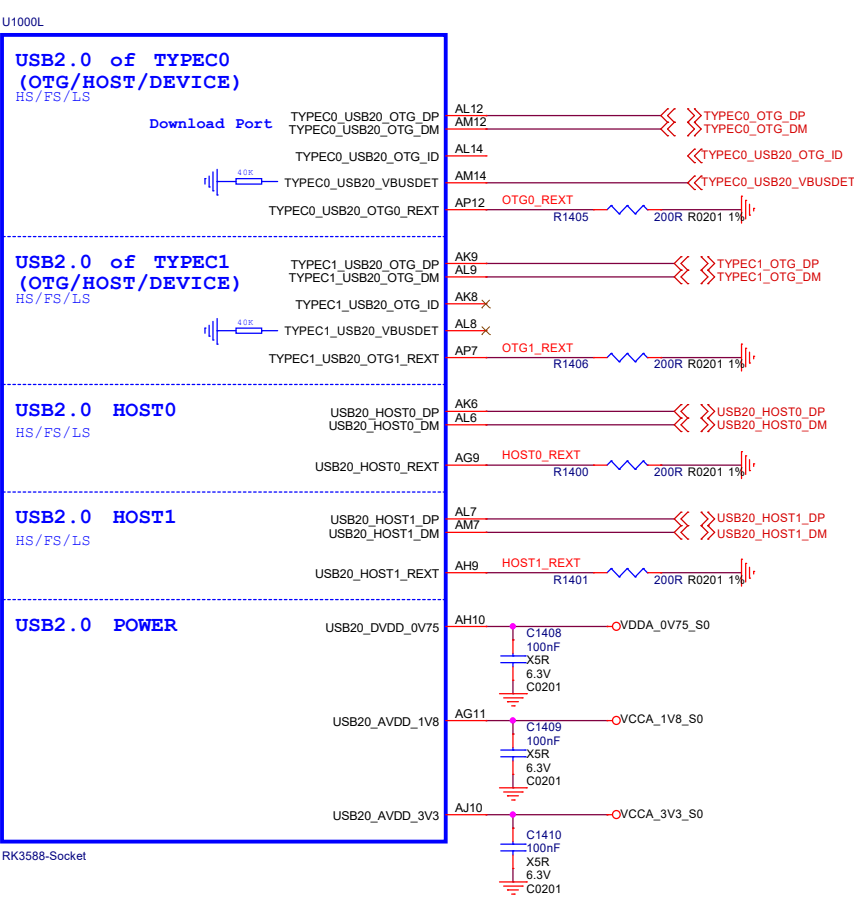


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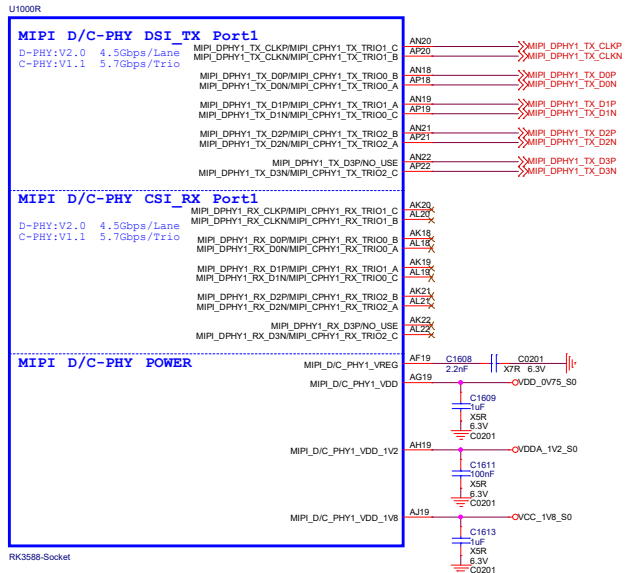
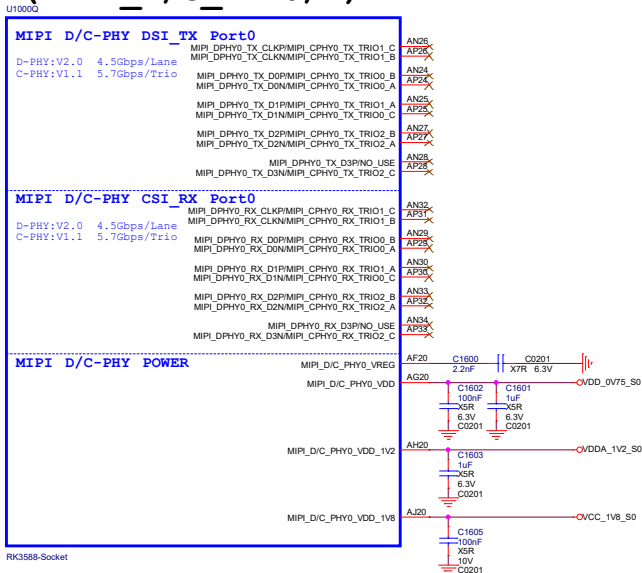
RK3588_M (TYPEC/DP)



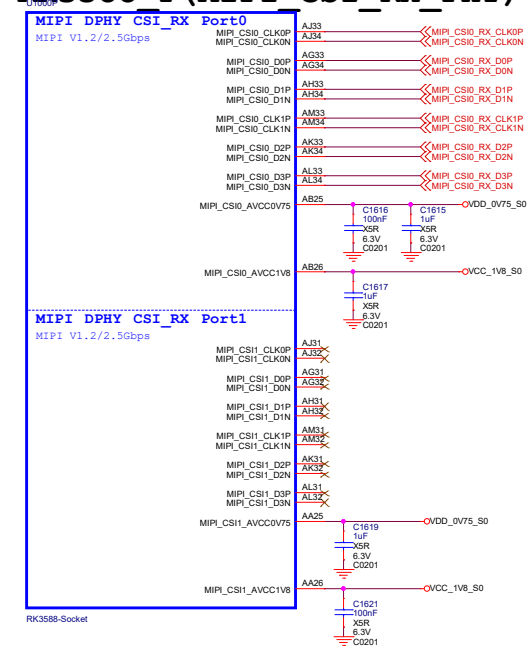
RK3588_L (USB2.0 HOST/OTG)



RK3588_Q/R(MIPI_D/C_PHY0/1)



RK3588_P(MIPI_CSI_RX_PHY)

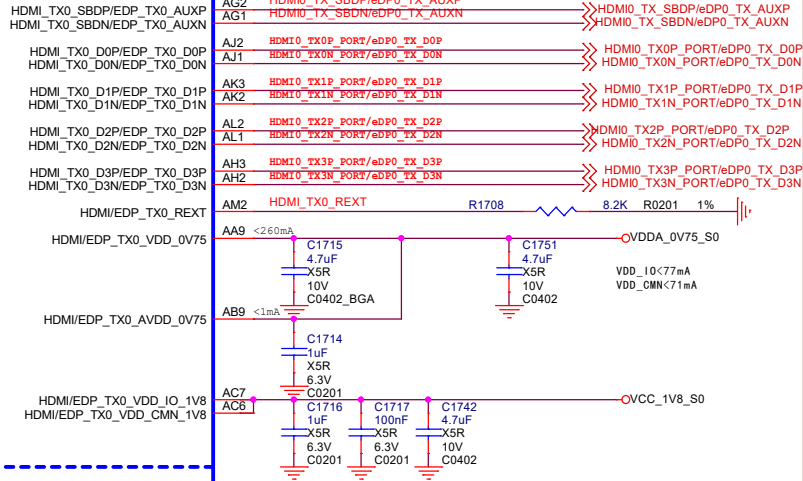


RK3588_S (HDMI2.1 TX)

U1000S

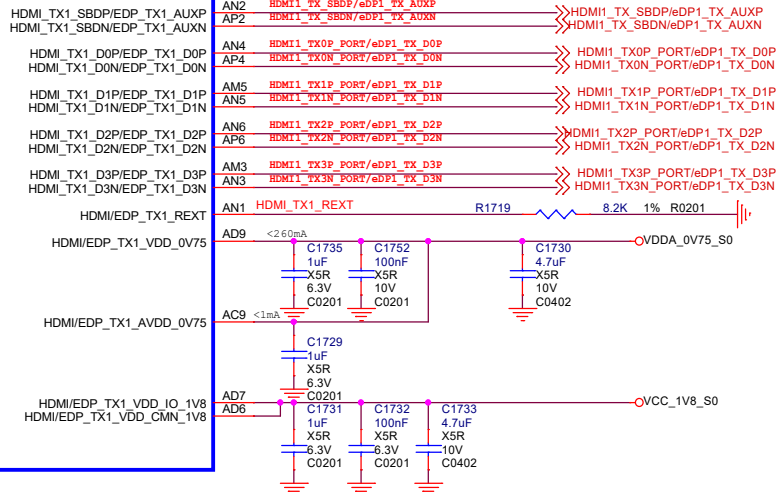
HDMI TX/eDP MUX Port0

HDMI:V2.1 12Gbps
eDP: V1.3 5.4Gbps



HDMI TX/eDP MUX Port1

HDMI:V2.1 12Gbps
eDP: V1.3 5.4Gbps



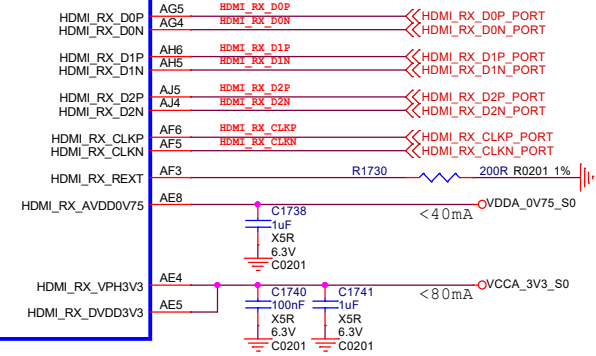
RK3588-Socket

RK3588 T (HDMI20 RX)

U1000T

HDMI RX

HDMI:V2.0

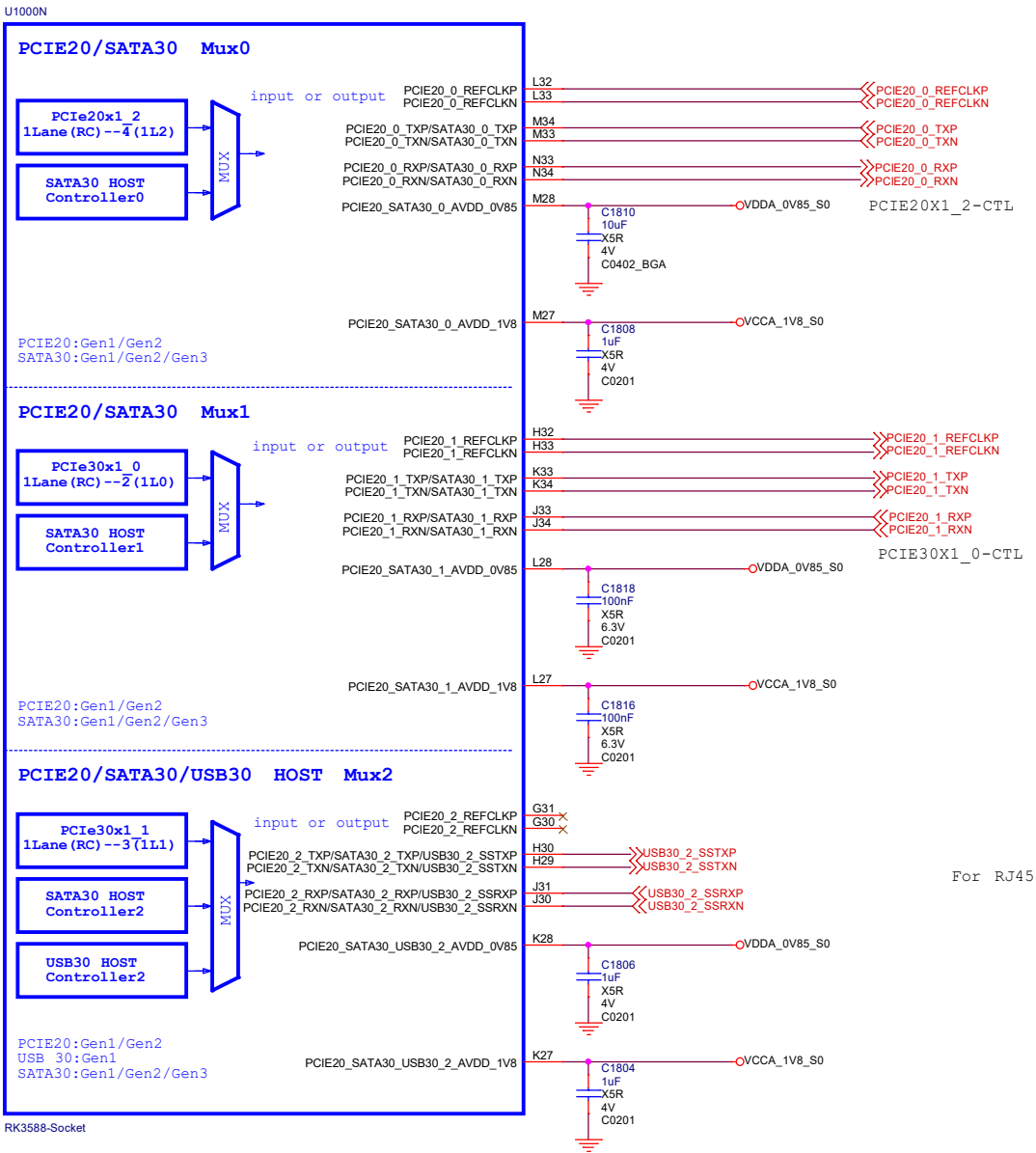


RK3588-Socket

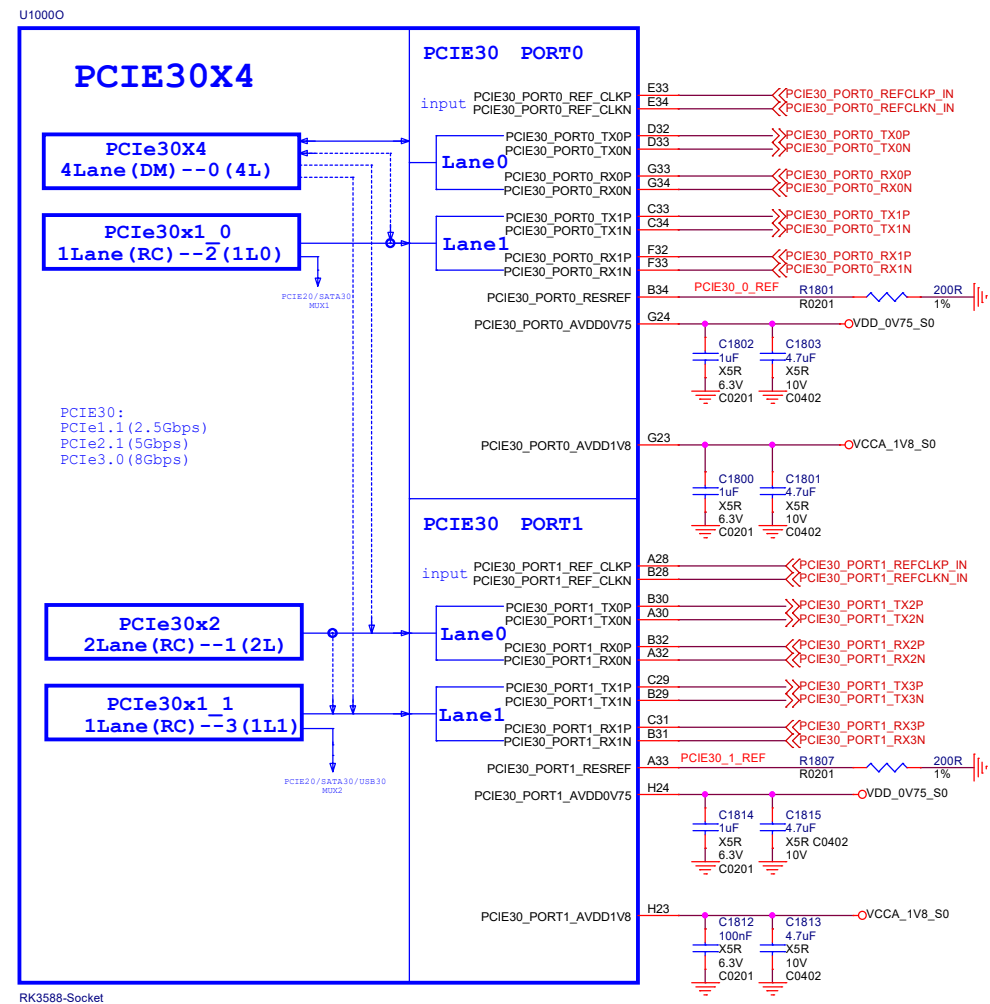
radxa

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RK3588_N (PCIE20)



RK3588_O (PCIE30)



VCCIO5 Domain										
Operating Voltage=1.8V/3.3V										
PNH01_M0	/ SPI4_M0IO_M0	/	/ I2C6_SDA_M0	/ FSPI_D0_M0	/ I2C6_SCL_M0	/ SDDIO_D0_M0	/ QNAC1_TWD0			GP003_A0_U0
ADDIO0_LN	/ SPI4_M0SI_M0	/ PNM01_TR_M0	/ I2C6_SCL_M0	/ FSPI_D1_M0	/ I2C6_SCL_M0	/ SDDIO_D1_M0	/ QNAC1_TWD3			GP003_A1_U0
ADDIO0_LP	/ SPI4_CLK_M0	/ UAR07_TX_M0	/	/ FSPI_D2_M0	/ I2C6_SCL_M0	/ SDDIO_D2_M0	/ QNAC1_RADD			GP003_A2_U0
ADDIO0_MP	/ SPI4_CSD_M0	/	/	/ FSPI_D3_M0	/ I2C6_SCL_M0	/ SDDIO_D3_M0	/ QNAC1_RADD			GP003_A3_U0
ADDIO0_RP	/ SPI4_C81_M0	/ UAR07_R0TN_M0	/	/	/ I2C6_SCL_M0	/ SDDIO_C8D0_M0	/ QNAC1_TWD1R			GP003_A4_d0
PNH01_M0	/ MIPI_CAMERA0_CLK_M0	/ UAR07_TX_M0	/ I2C6_SDA_M0	/ FSPI_CLK_M0	/	/ SDDIO_C8D1_M0	/ QNAC1_RADD			GP003_A5_d0
	/ MIPI_CAMERA0_CLK_M0	/	/ I2C4_SCL_M0	/	/	/	/ R0TBL_REFCLKD_25M			GP003_A6_d0
PNH01_M0	/ MIPI_CAMERA0_CLK_M0	/	/	/	/	/	/ QNAC1_RADD			GP003_A7_U0
PNH01_M0	/ MIPI_CAMERA0_CLK_M0	/ UAR07_TX_M0	/	/	/	/	/ QNAC1_RADD_C8D0			GP003_B0_d0
PNH01_TR_M0	/ MIPI_CAMERA0_CLK_M0	/ UAR07_RX_M0	/	/	/	/	/ QNAC1_TYER			GP003_B2_d0
	/	/ UAR07_R0TN	/	/	/ I2C6_SCL_M0	/	/ QNAC1_TWD0			GP003_B3_U0
PNH01_M0	/	/ UAR07_C7D0	/	/	/ I2C6_SCL_M0	/	/ QNAC1_TWD0			GP003_B4_U0
PNH01_M0	/ CAN1_RX_M0	/ UAR07_TX_M0	/	/	/ I2C6_SCL_M0	/	/ QNAC1_TYER			GP003_B5_U0
PNH01_M0	/ CAN1_TX_M0	/ UAR07_RX_M0	/	/	/ I2C6_SCL_M0	/	/ QNAC1_M0CIN0T0			GP003_B6_d0
	/	/	/ I2C6_SCL_M0	/ SPI1_M0SI_M0	/ H0MI_TX1_WPD_M0	/	/ QNAC1_PTP_REF_CLK			GP003_B7_d0
	/	/ UAR07_TX_M0	/ I2C3_SDA_M0	/ SPI1_M0IO_M0	/	/	/ QNAC1_P0PFRIG			GP003_C0_d0
PNH01_M0	/ P0C10002_0_BUTTON_R0TN	/ UAR07_RX_M0	/	/ SPI1_C8D1_M0	/	/	/ QNAC1_P0PFRIC			GP003_C1_d0
PNH01_TR_M0	/	/ UAR07_R0TN_M0	/ I2C6_SCL_M0	/ SPI1_C8D0_M0	/	/ MIPI_T0D0	/ QNAC1_M0			GP003_C2_d0
PNH01_RX_M0	/	/ UAR07_C7D0_M0	/ I2C6_SDA_M0	/ SPI1_C81_M0	/	/ MIPI_T0E1	/ QNAC1_M0IO			GP003_C3_d0
CAN1_RX_M0	/ P0C10004_CLEARN0_M0	/ UAR07_TX_M0	/ FSPI_C7D0_M0	/ SPI3_C8D0_M0	/ H0MI_TX1_C8D0_M0	/	/ C1P_D8			GP003_C4_U0
CAN1_TX_M0	/ P0C10004_WAREN0_M0	/ UAR07_RX_M0	/ FSPI_C81D0_M0	/ SPI3_C81_M0	/ H0MI_TX1_SDA_M0	/	/ C1P_D9			GP003_C5_U0
	/ P0C10004_P0PFRIG_M0	/	/	/ SPI3_M0IO_M0	/ H0MI_TX1_SCL_M0	/	/ C1P_D10			GP003_C6_U0
	/ P0C10001_2_CLEARN0_M0	/	/ I2C6_SCL_M0	/ SPI3_M0SI_M0	/ H0MI_TX1_SCL_M0	/	/ C1P_D11			GP003_C7_U0
PNH01_M0	/ P0C10001_2_WAREN0_M0	/ UAR07_RX_M0	/ I2C6_SDA_M0	/ SPI3_C1D0_M0	/ H0MI_TX1_SDA_M0	/	/ C1P_D12			GP003_D0_U0
PNH01_M0	/ P0C10001_2_P0PFRIG_M0	/ UAR07_TX_M0	/	/ SPI0_M0IO_M0	/ H0MI_RX_C8D0_M0	/	/ C1P_D13			GP003_D1_d0
	/ P0C10002_CLEARN0_M0	/ UAR07_R0TN_M0	/ I2C7_SCL_M0	/ SPI0_M0SI_M0	/ H0MI_RX_SCL_M0	/	/ C1P_D14			GP003_D2_d0
	/ P0C10002_P0PFRIG_M0	/ UAR07_C7D0_M0	/	/	/ SPI0_C8D0_M0	/	/ C1P_D15			GP003_D3_d0
	/ P0C10002_P0PFRIG_M0	/ UAR07_RX_M0	/	/	/ SPI0_C8D0_M0	/ H0MI_RX_WPD00T_M0	/ H0MI_TX1_WPD_M0	/ M0U_JTAG_T0E1_M0		GP003_D4_d0
PNH01_TR_M0	/ P0C10004_0_BUTTON_R0TN	/ UAR07_TX_M0	/	/	/ SPI0_C81_M0	/ SPI1_WPD00T_M0	/	/ M0U_JTAG_T0E1_M0		GP003_D5_d0

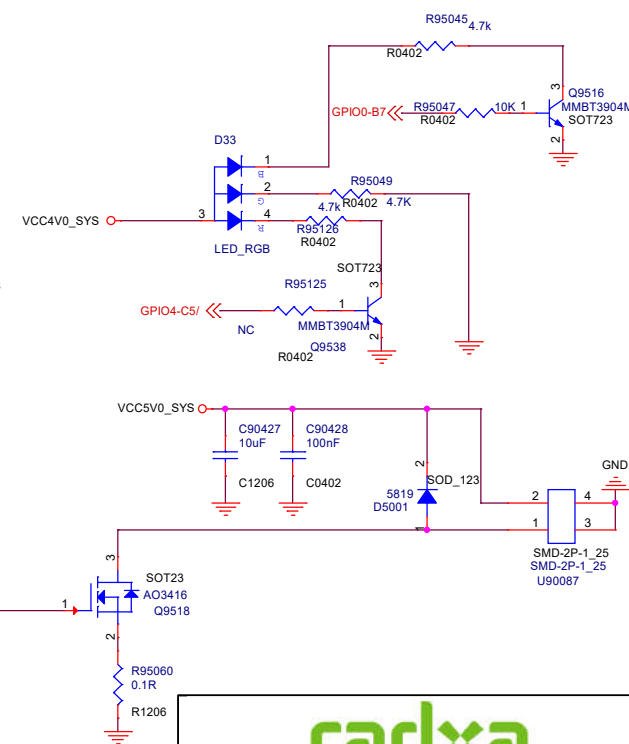
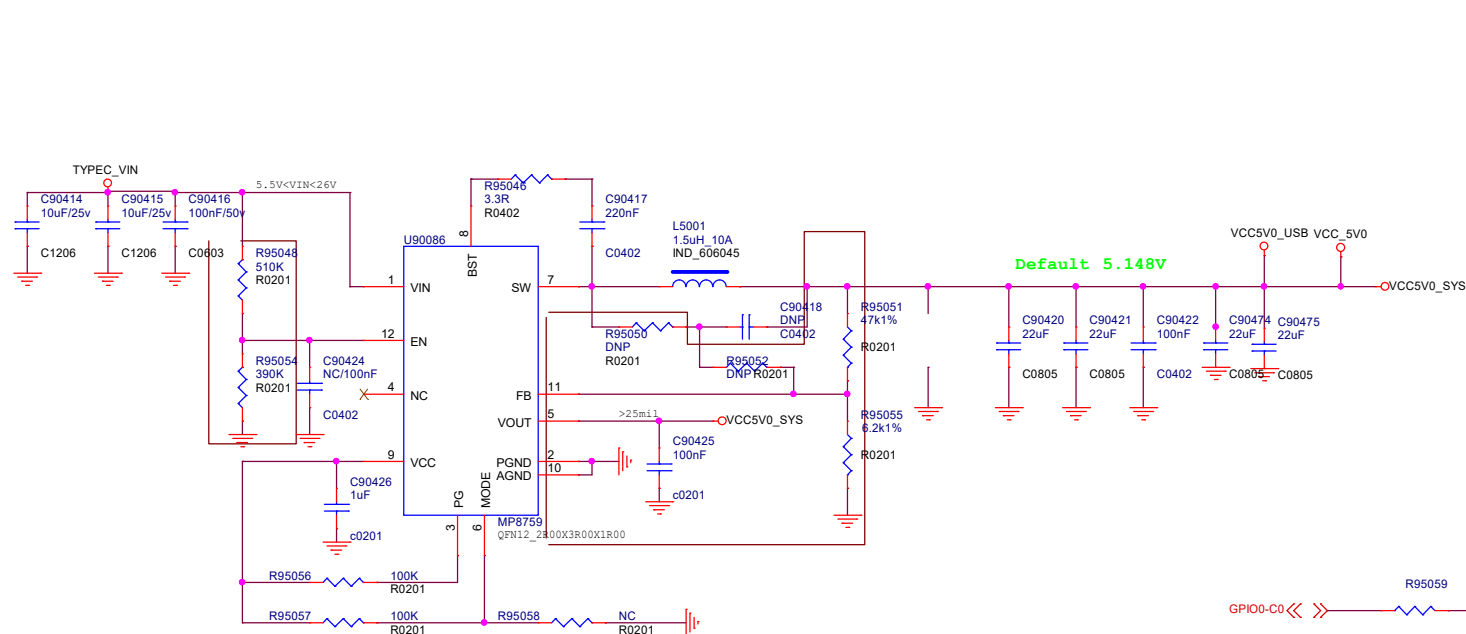
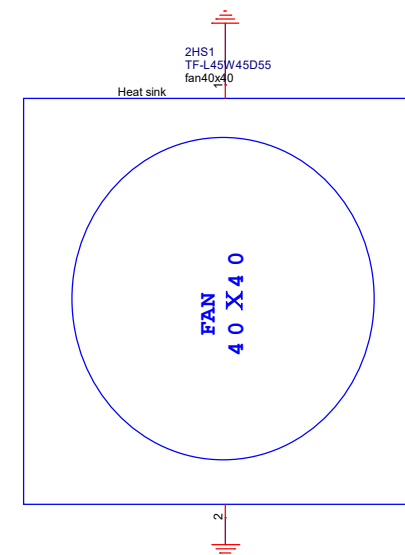
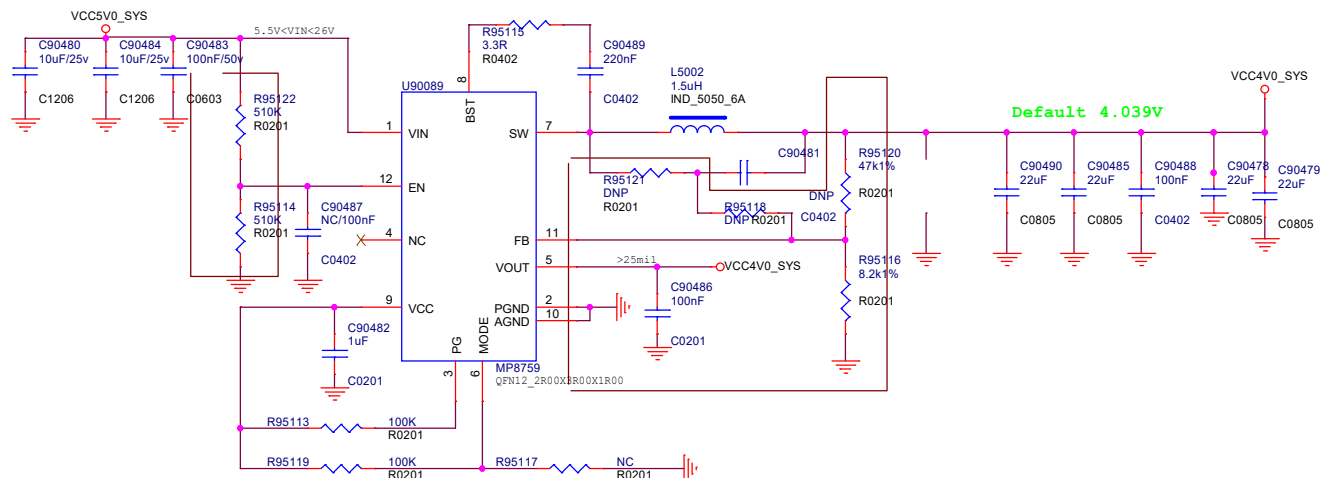
VCCIO5_V18

VCCIO5

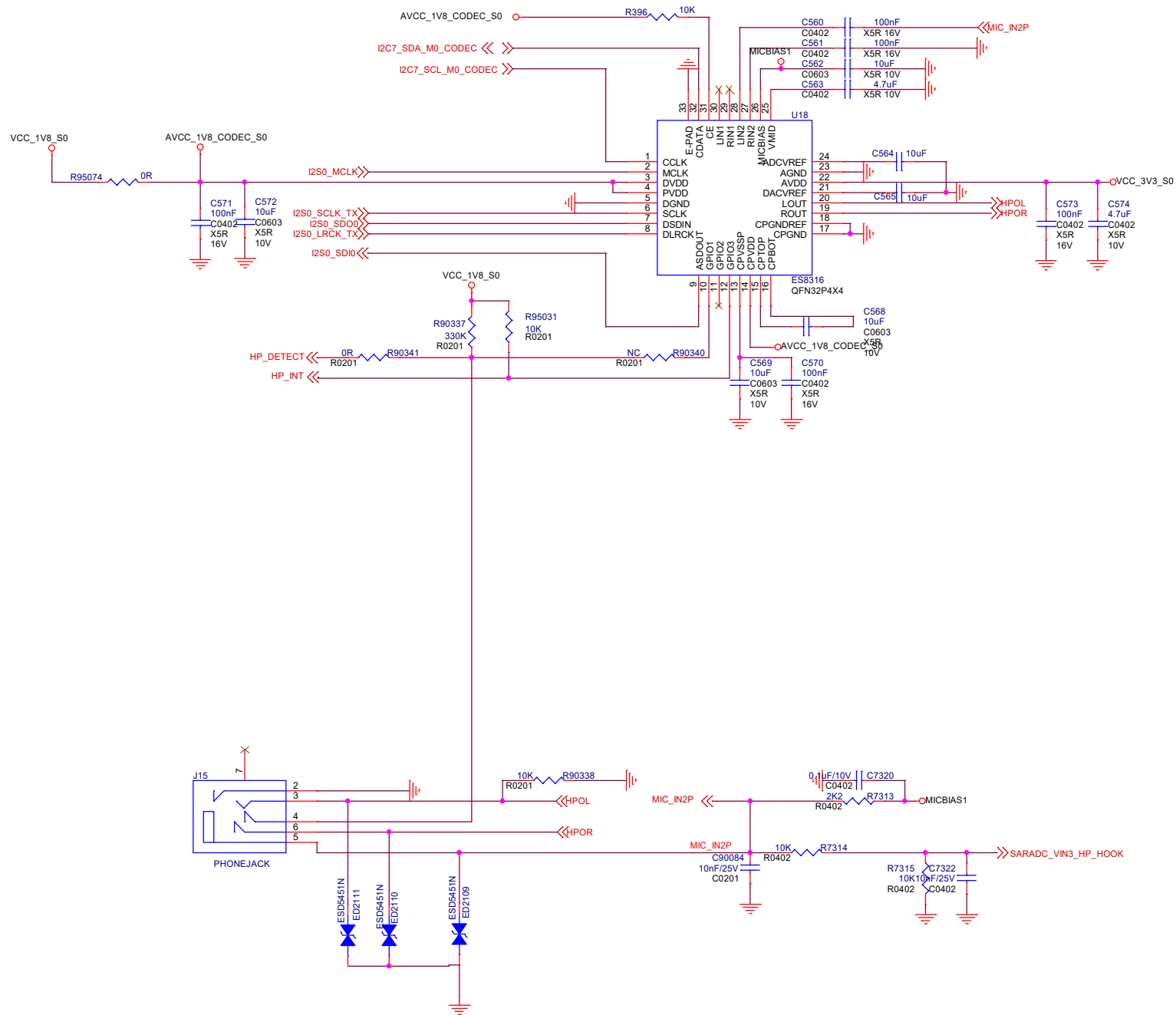
UPOOK																				
VCIO06 Domain																				
Operating Voltage=1.8V/3.3V																				
	/ SPI0 MIS0 M1	/ UART5 RXEN M1	/ I2S1 MCLK M0	/ PCIE30X1_1 CLASREQ M1	/ BT1120 D0	/ CIF D0				GP00A A0										
	/ SPI0 MOSI M1	/ UART5 CTEN M1	/ I2S1 SCLK M0	/ PCIE30X1_1 WAKEN M1	/ BT1120 D1	/ CIF D1				GP00A A1										
	/ SPI0 CLK M1		/ I2S1 LACK M0	/ PCIE30X1_1 PERSTN M1	/ BT1120 D2	/ CIF D2				GP00A A2										
		/ UART5 TX M2		/ PCIE30X1_0 CLASREQ M1	/ BT1120 D3	/ CIF D3				GP00A A3										
	/ SPI2 MIS0 M1	/ UART5 RX M2	/ I2C3 SCL M2		/ PCIE30X1_0 WAKEN M1	/ BT1120 D4	/ CIF D4			GP00A A4										
	/ SPI2 MOSI M1	/ UART5 TX M2	/ I2C3 SDA M2	/ I2S1 SDIO M0	/ PCIE30X1_0 PERSTN M1	/ BT1120 D5	/ CIF D5			GP00A A5										
	/ SPI2 CLK M1	/ UART5 RX M2	/ I2C3 SCL M2	/ I2S1 SD11 M0	/ PCIE30X2 CLASREQ M1	/ BT1120 D6	/ CIF D6			GP00A A6										
	/ SPI2 CS0 M1		/ I2C3 SDA M2	/ I2S1 SDIO M2	/ PCIE30X2 WAKEN M1	/ BT1120 D7	/ CIF D7			GP00A A7										
	/ SPI2 CS1 M1	/ UART5 TX M0	/ I2C3 SDA M3	/ I2S1 SD13 M0	/ PCIE30X2 PERSTN M1	/ BT1120 CLASREQ	/ CIF CLASREQ			GP00A B0										
	BATA2 ACT LED M0	/ SPIOFI TX M1	/ SPI0 CS1 M1	/ UART5 RX M0	/ I2C4 SCL M3	/ I2S1 SDIO M0		/ PCIE30X1_0 BUTTON RSTN F	/ MIPI CAMERA0D CLK M0	GP00A B1										
	CAN0 RX M1	/ PNM0A M1	/ SPI0 CS0 M1	/ UART5 RXEN M0	/ I2C7 SCL M3	/ I2S1 SDIO1 M1		/ PCIE30X1_1 BUTTON RSTN F	/ BT1120 D8	GP00A A8										
	CAN0 TX M1	/ PNM0S IR M1																		
	CAN1 TX M1		/ UART5 CTEN M0	/ I2C7 SDA M3	/ I2S1 SDIO2 M0	/ PCIE20X1_2 BUTTON RSTN F	/ BT1120 D9	/ CIF VSYNC		GP00A B3										
	SPIOFI TX M1	/ PNM0L TX M1	/ SPIO TX M1			/ PCIE30X2 CLASREQ M1	/ BT1120 D10	/ CIF CLASREQ		GP00A B4										
	BATA1 ACT LED M0	/ PNM0L2 M1	/ SPI3 MIS0 M1	/ UART5 RX M1		/ HDMI RX CRC M0	/ PCIE30X4 WAKEN M1	/ BT1120 D11		GP00A B5										
	BATA0 ACT LED M0	/ PNM0L1 M1	/ SPI3 MOSI M1		/ I2C5 SCL M1	/ HDMI RX HPOOF M0	/ PCIE30X4 PERSTN M1	/ BT1120 D12		GP00A B6										
			/ SPI3 CLK M1		/ I2C5 SDA M1	/ HDMI TX0 SCL M0	/ PCIE20X1_2 CLASREQ M1	/ BT1120 D13		GP00A B7										
			/ SPI3 CS0 M1		/ I2C6 SCL M3	/ HDMI TX0 SDA M0	/ PCIE20X1_2 WAKEN M1	/ BT1120 D14		GP00A C0										
	SPIOFI TX M2	/ PNM0B M1	/ SPI3 CLK M1		/ I2C6 SDA M3	/ HDMI TX1 SCL M0	/ PCIE20X1_2 PERSTN M1	/ BT1120 D15		GP00A C1										

VCCIO4 Domain	
Operating Voltage=1.8V/3.3V	
.....	
.....	A24
.....	A25
.....	A26
.....	A27
.....	B25
.....	B26
.....	C24
.....	C25
.....	C27
.....	D25
.....	D26
.....	D27
.....	E24
.....	E25
.....	E26
.....	F24
.....	F25
.....	H20
.....	H21
VCCIO4_1V8	
.....	
VCCIO4	

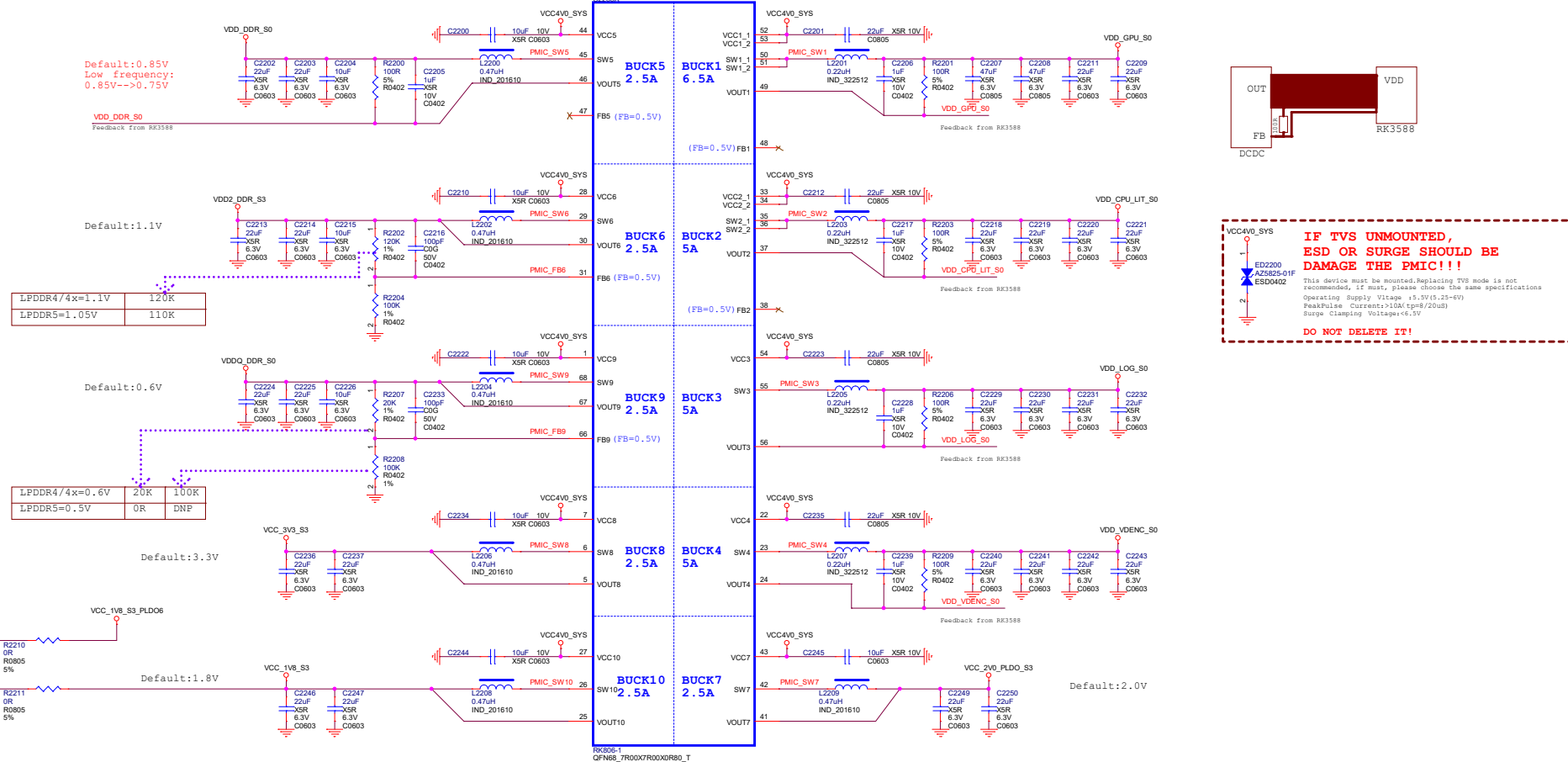
TYPEC-IN ----> **MP8759** **DCDC** **5.148V** ----> **USB**
 ----> **VCC5V0** ----> **MP8759** **DCDC** **4.039V** ----> **PMU**
 ----> **40PIN**



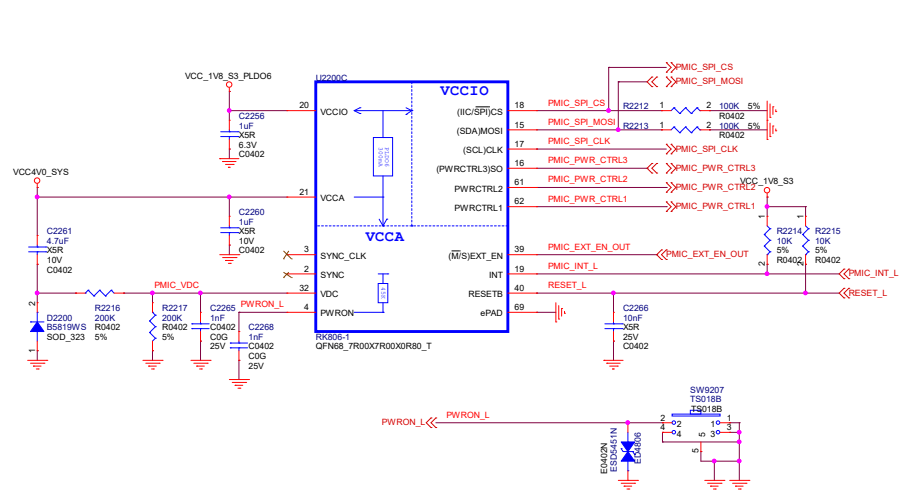
CODEC ES8316



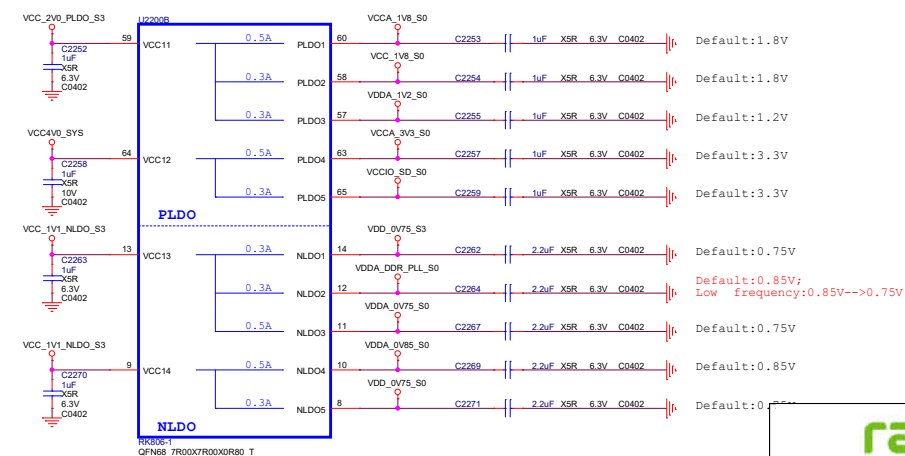
PMIC RK806-1 BUCK



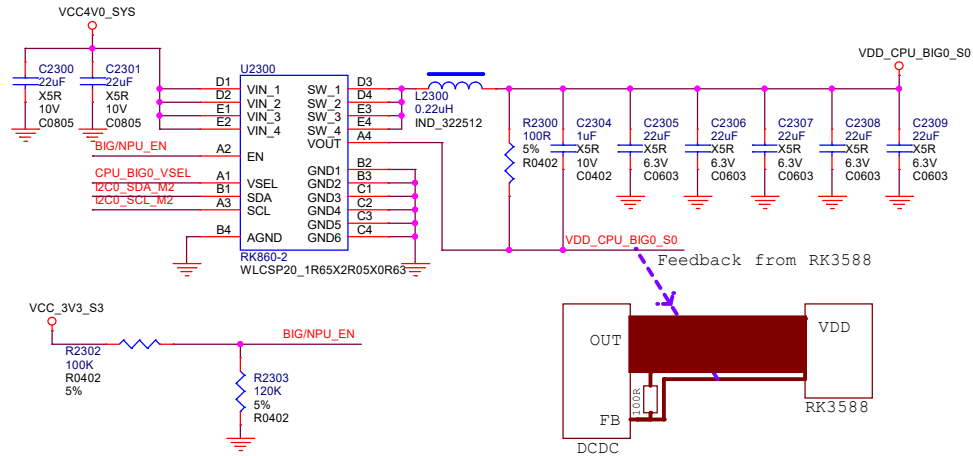
PMIC RK806-1 Managerment



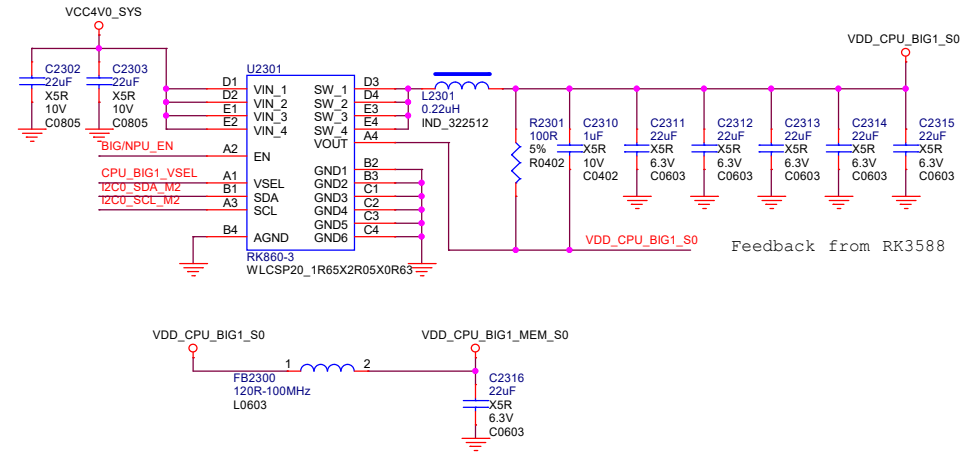
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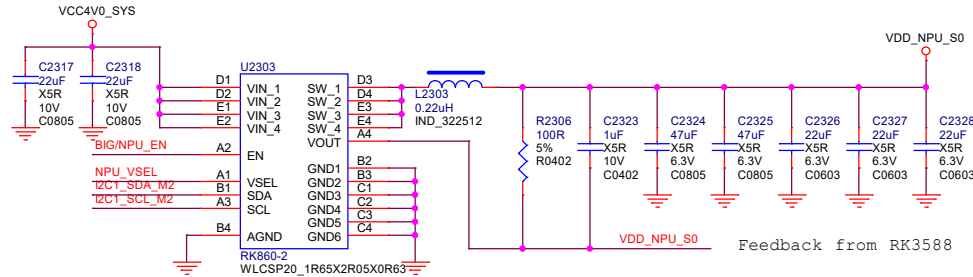
VDD_CPU_BIG0



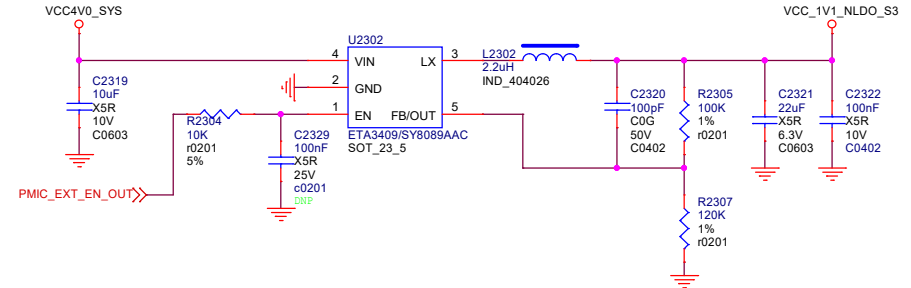
VDD_CPU_BIG1



VDD_NPU



VCC_1V1_NLDO_S3



I2C1_SCL_M2

I2C1_SDA_M2

I2C0_SCL_M2

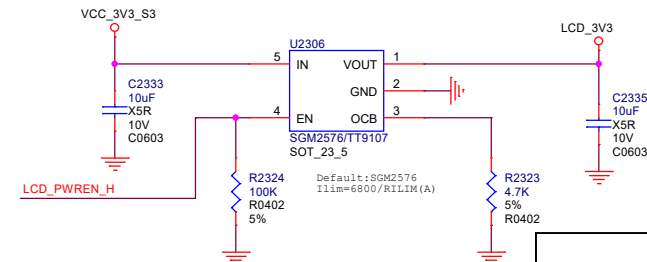
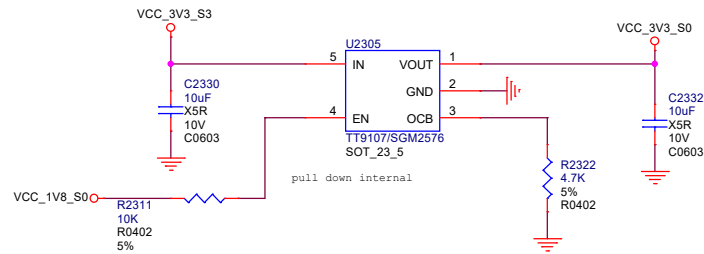
I2C0_SDA_M2

CPU_BIG0_VSEL

CPU_BIG1_VSEL

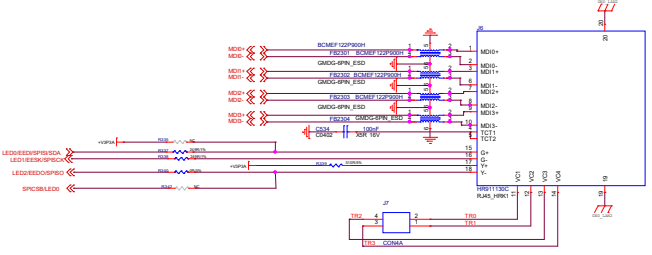
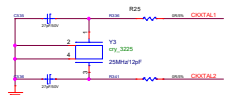
NPU_VSEL

LCD_PWREN_H

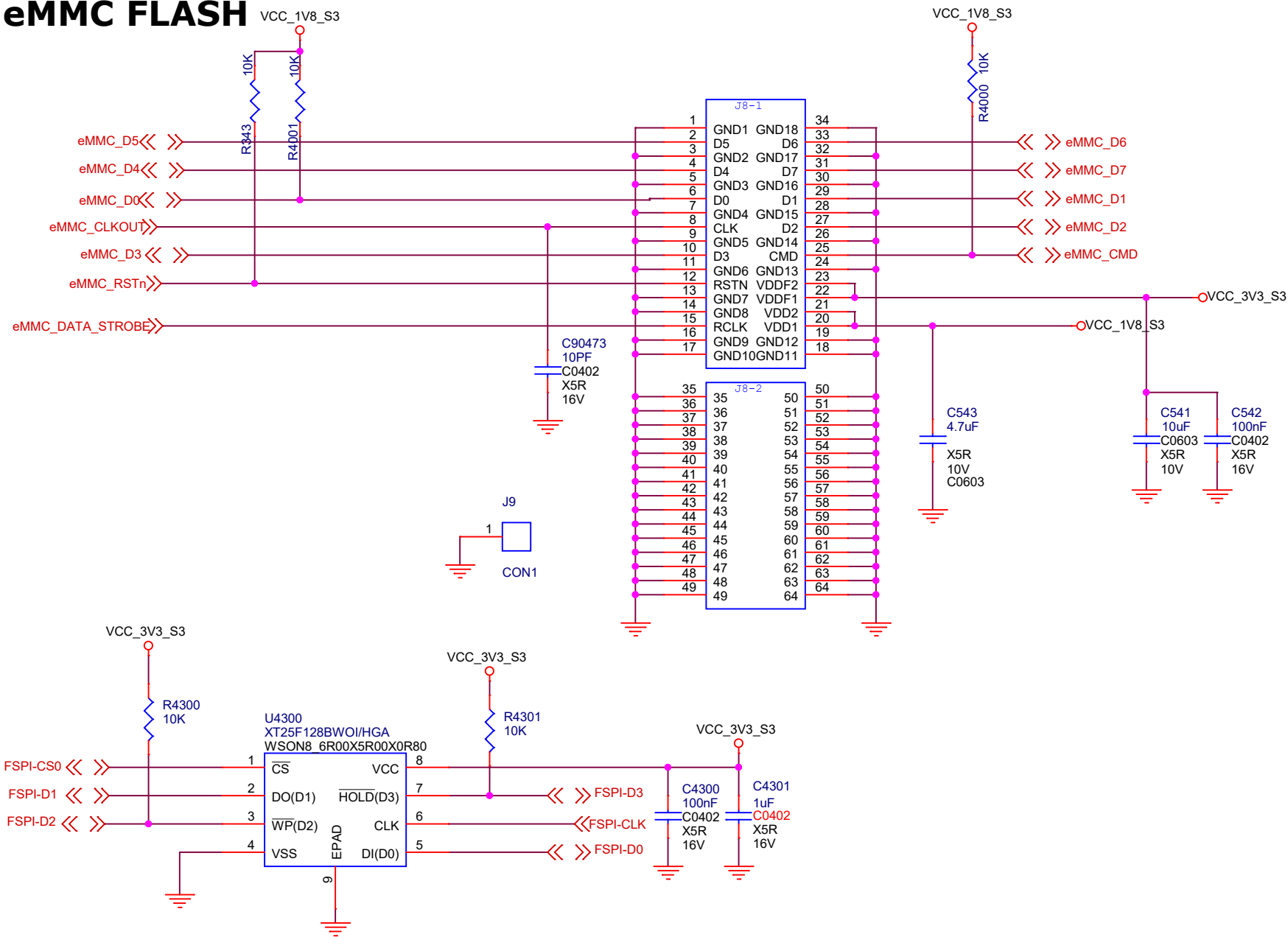


radxa

Size	Title:	REV
A3	ROCK 5B	1.45
	Page Name: Power Ext Discrete	
Date: Tuesday, April 25, 2023	Sheet 23 of 32	

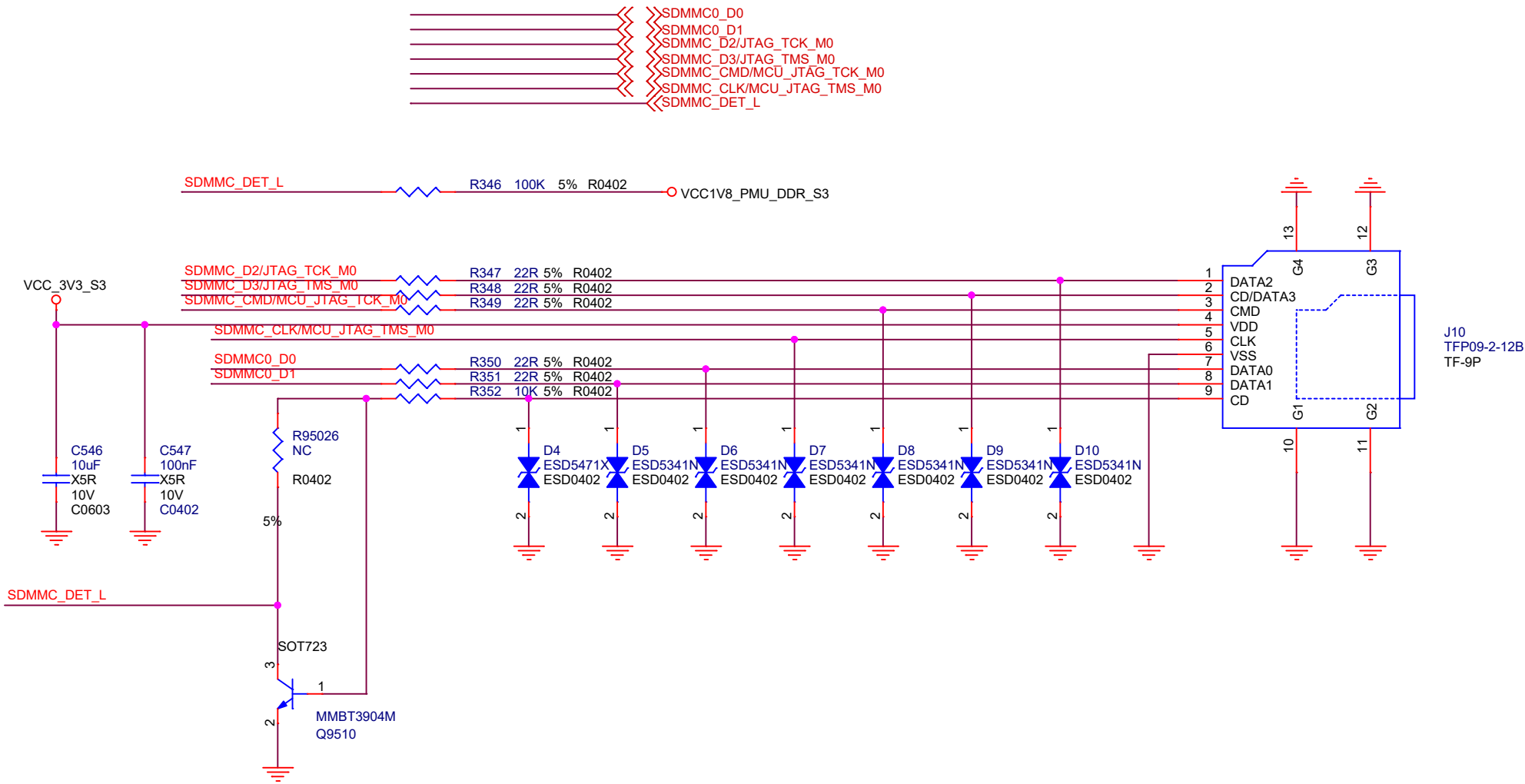


eMMC FLASH

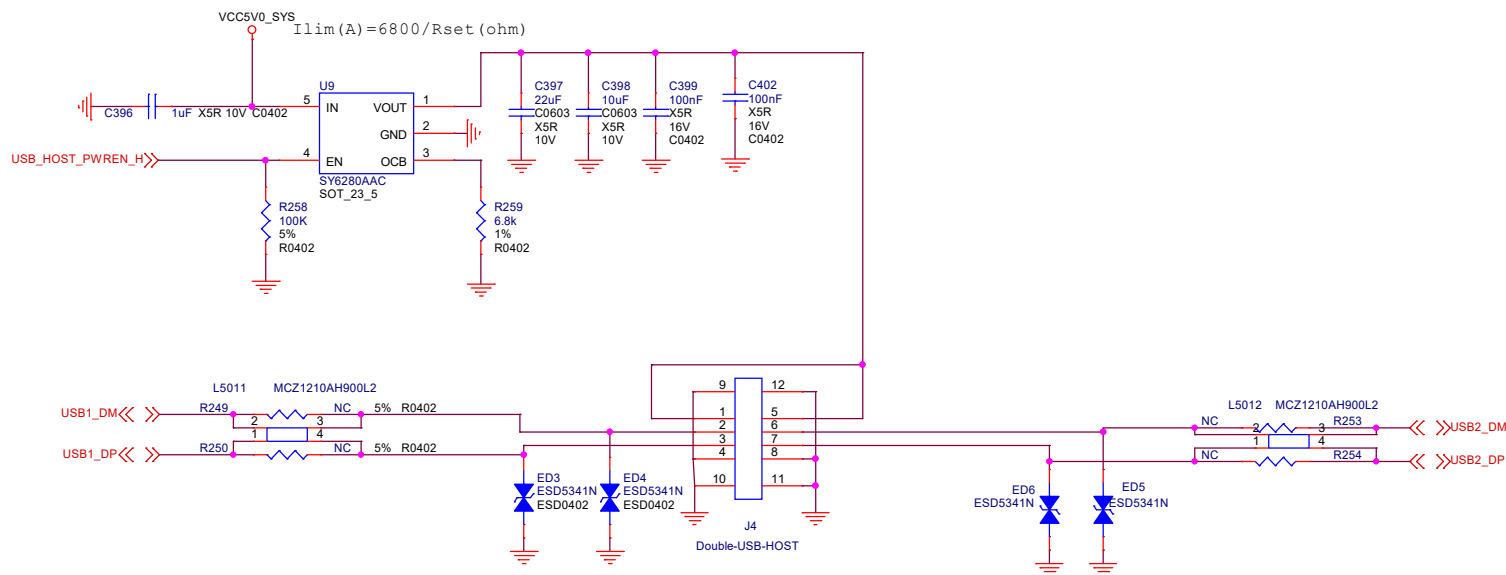


radxa		
Size	Title: ROCK 5B	REV
A4	Page Name: eMMC FLASH	1.45
Date: Tuesday, April 25, 2023	Sheet 25 of 32	

TF CARD



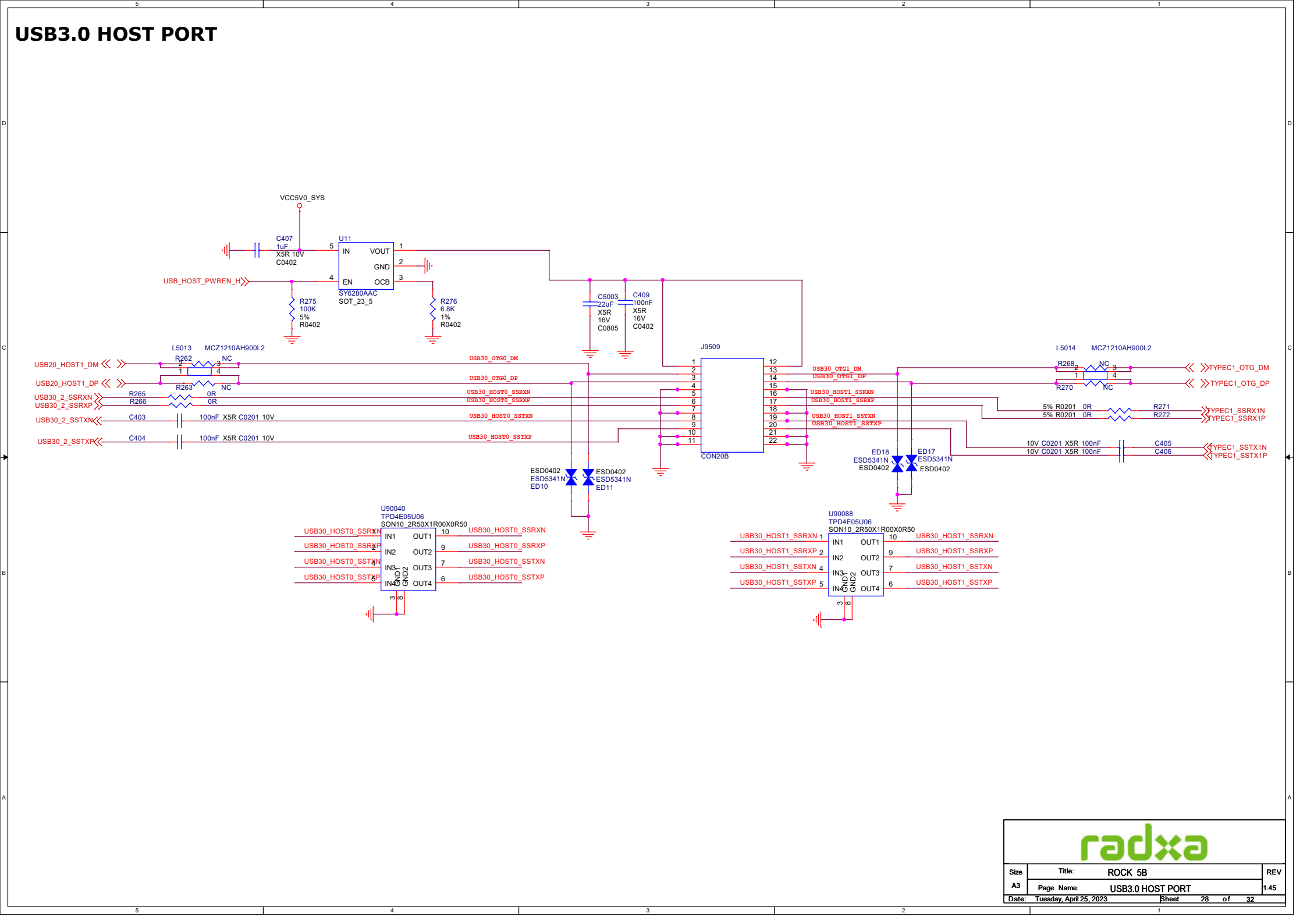
USB2.0



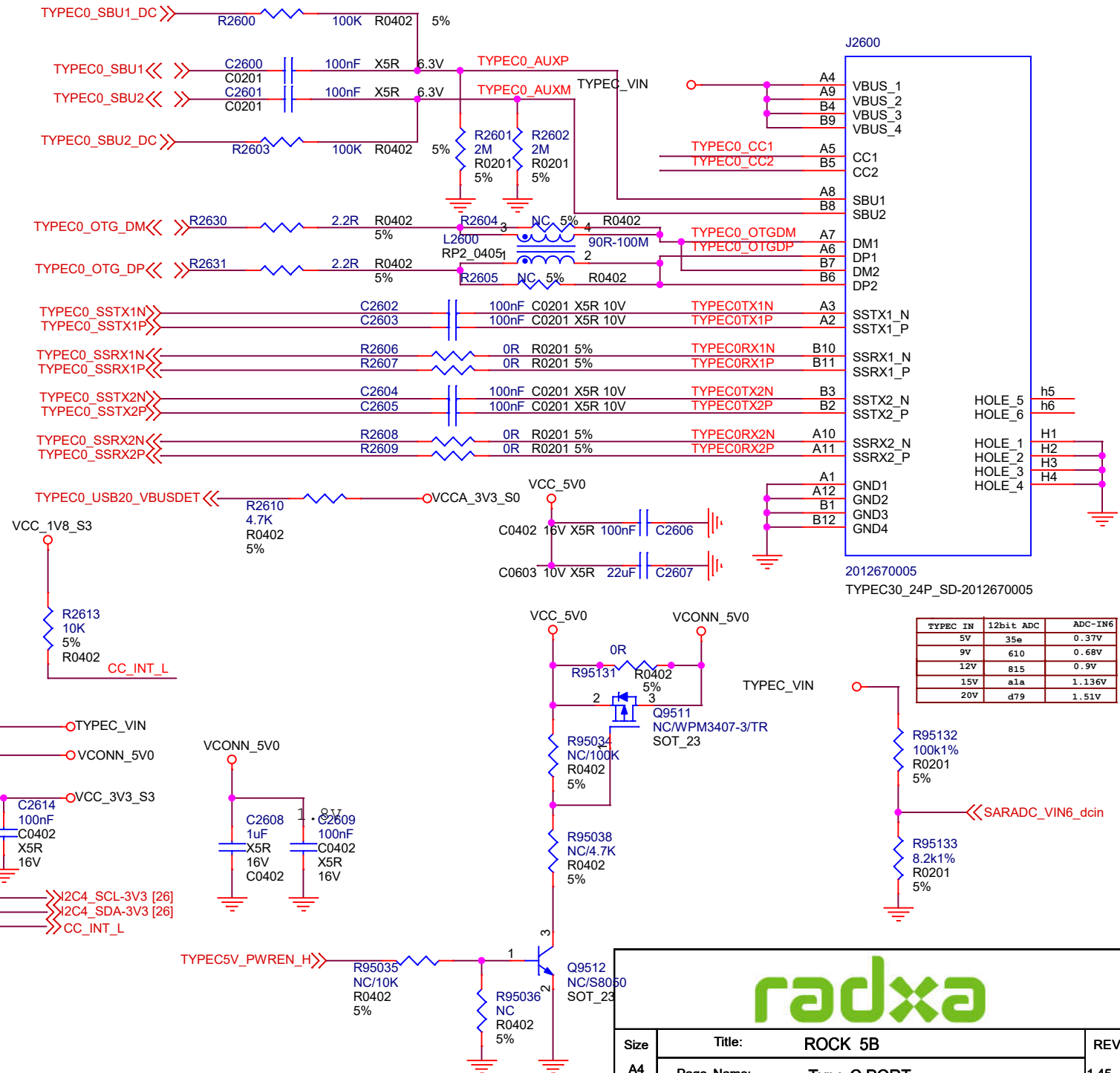
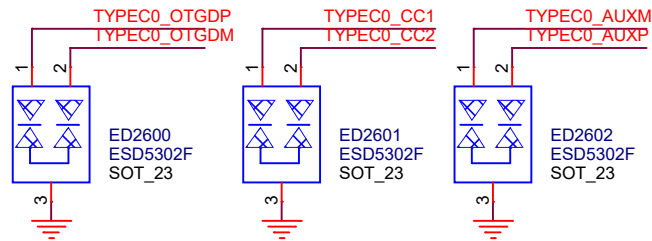
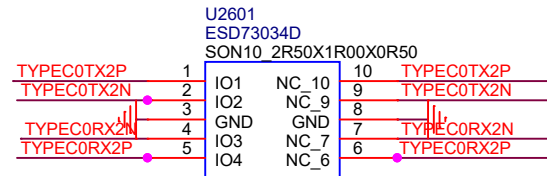
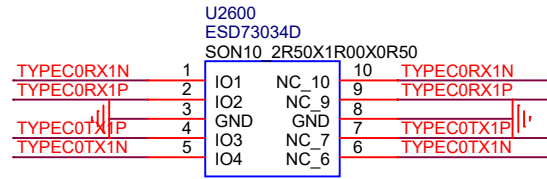
radxa

Size	Title: ROCK 5B	REV
A3	Page Name: USB2.0	1.45
Date: Tuesday, April 25, 2023	Sheet 27 of 32	

USB3.0 HOST PORT



Type-C PORT



TYPE	IN	12bit ADC	ADC-IN6
5V	35e	0.37V	
9V	610	0.68V	
12V	815	0.9V	
15V	a1a	1.136V	
20V	d79	1.51V	

radxa

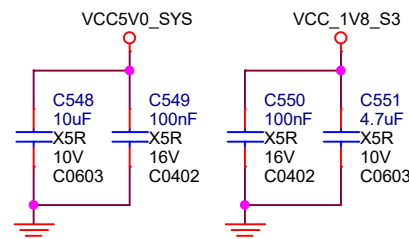
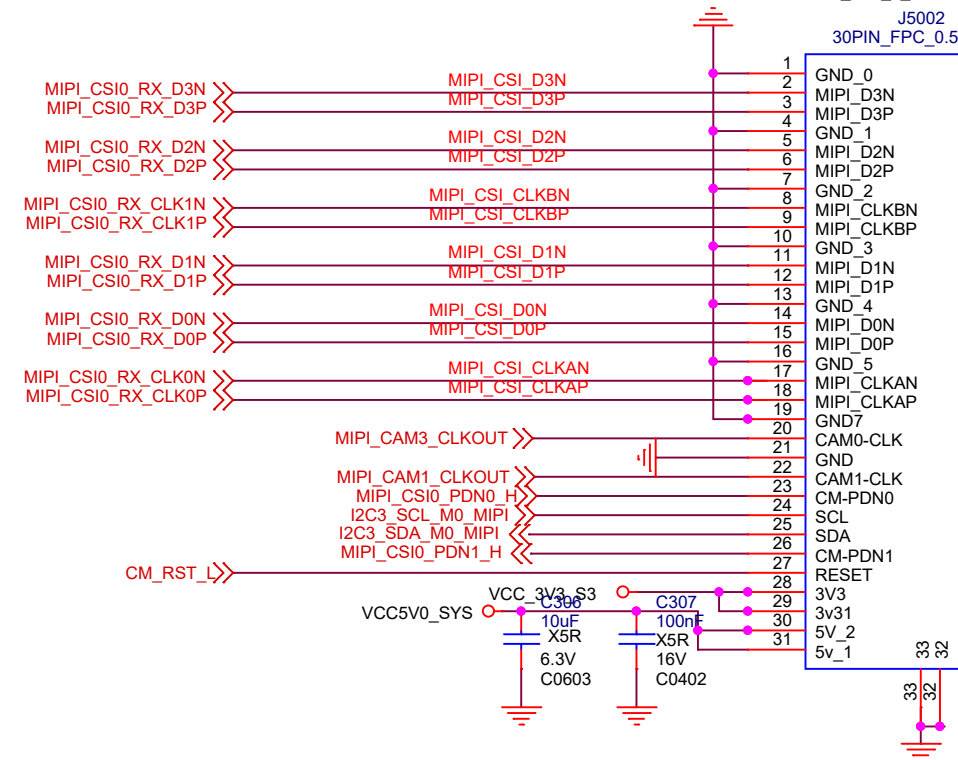
Size	Title: ROCK 5B	REV
A4	Page Name: Type-C PORT	1.45
Date:	Tuesday, April 25, 2023	Sheet 29 of 32

CAM

FH35C_31P_0_6SHW

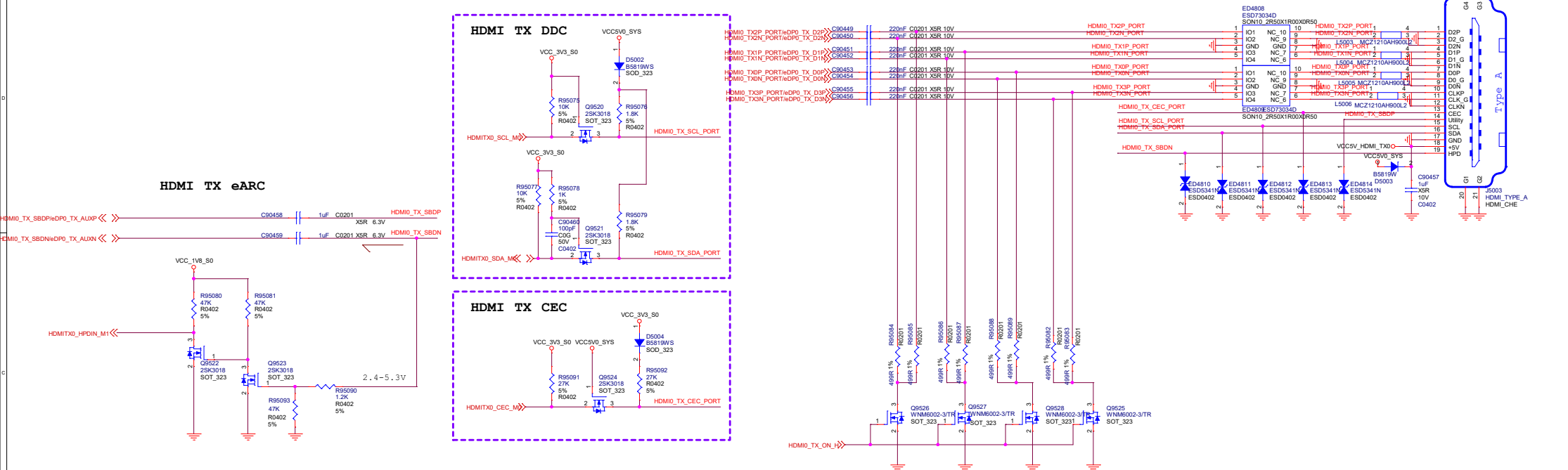
J5002

30PIN_FPC_0.5

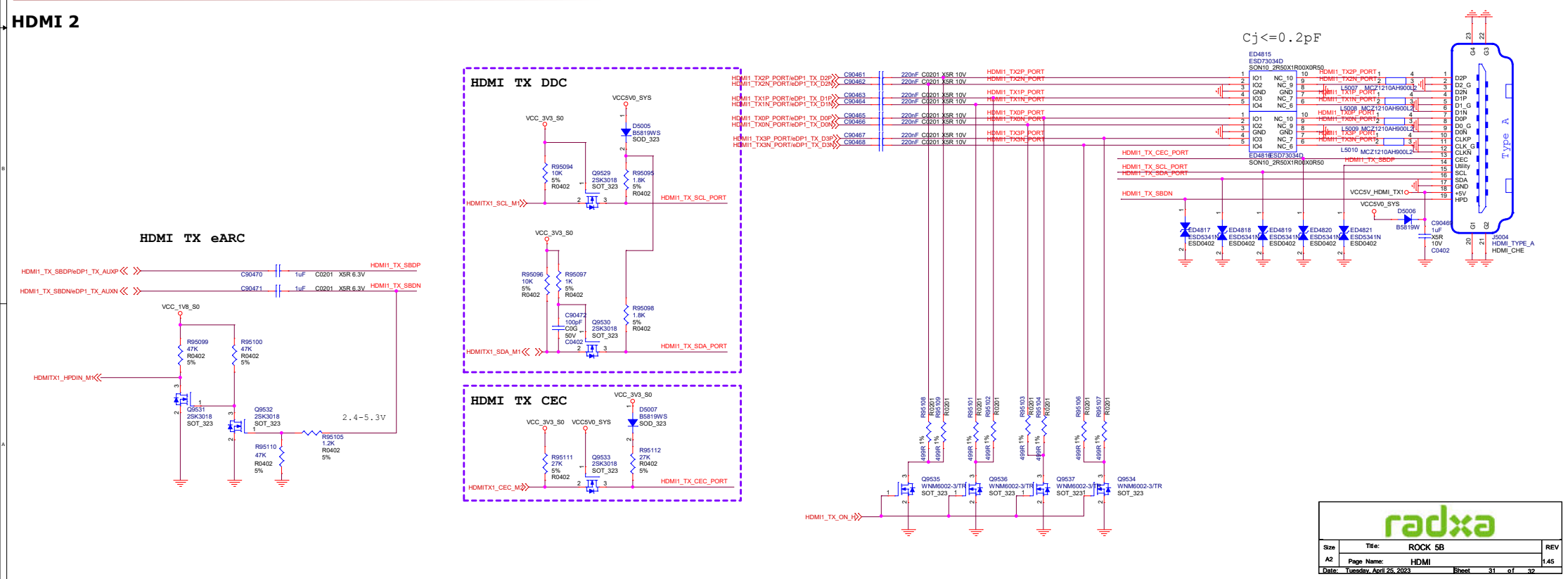


		
Size	Title: ROCK 5B	REV
A4	Page Name: CAM	1.45
Date: Tuesday, April 25, 2023	Sheet 30 of 32	

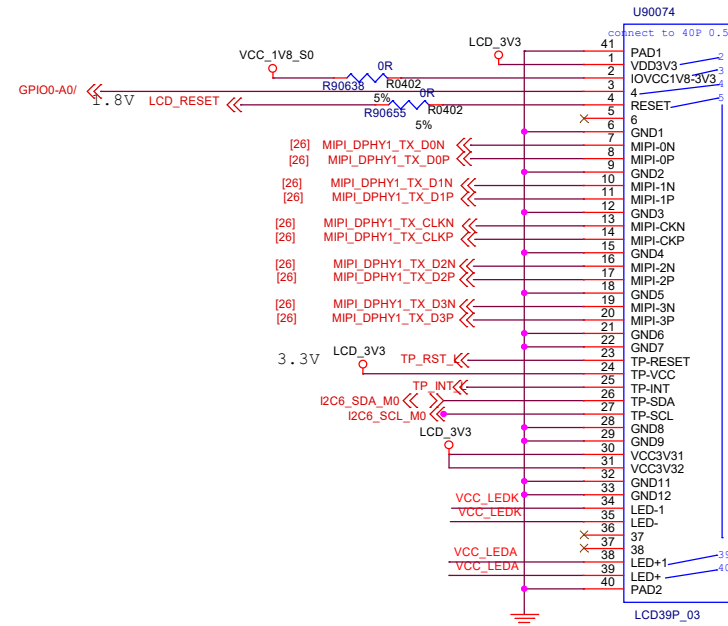
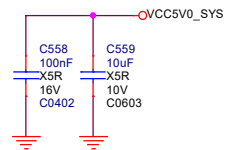
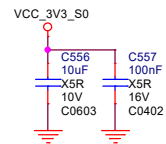
HDMI 1



HDMI 2



MIPI DPHY1 TX



LCD

